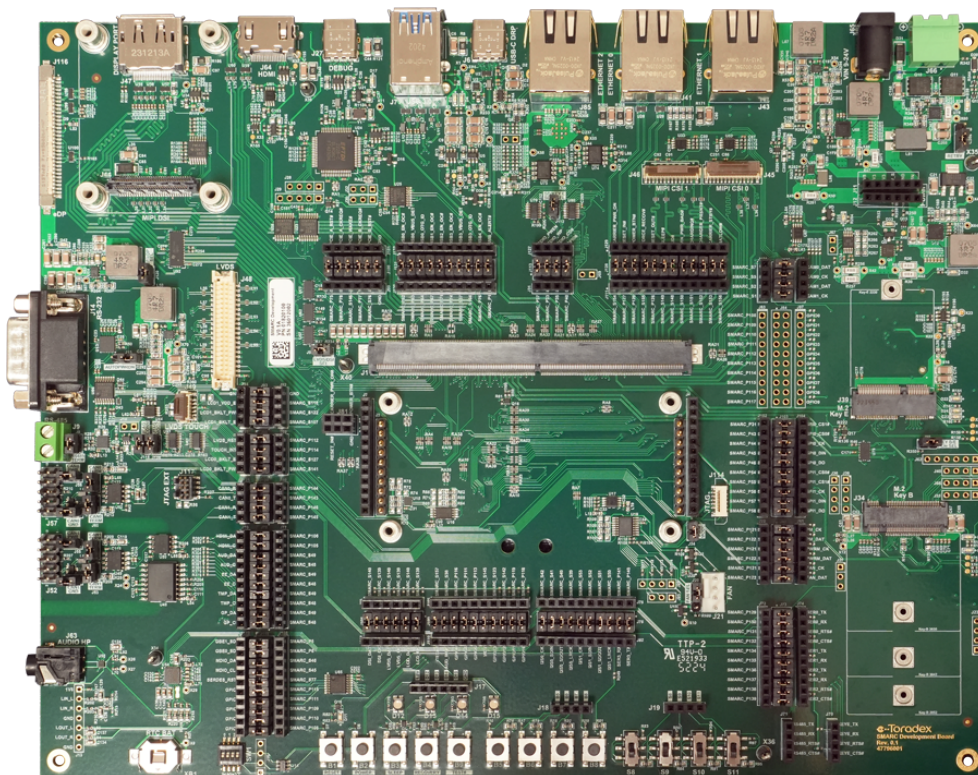


SMARC Development Board

HW Errata



Revision History

Document Revisions

| Date | Doc. Revision | Product Version | Changes |
|-------------|---------------|------------------|-----------------------------------|
| 27-Feb-2026 | Rev. 0.1 | V1.0 | Initial documentation |
| 24-Mar-2026 | Rev. 0.2 | V1.0, V1.1, V1.2 | Section 3 : added |

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1 Errata #1: HAR-12360 – The DP connector is conflicting with the Toradex DSI to LVDS adapter

Affected version:

SMARC Development Board V1.1

Fixed in:

SMARC Development Board V1.2

1.1 Customer Impact

Customers can not use Toradex DSI to LVDS Adapter with SMARC Development Board.

1.2 Description

The C4 capacitor of the Toradex DSI to LVDS adapter is too high and collides with the DP connector on SMARC Development Board.

1.3 Workaround

There is no available workaround. Issue was fixed on SMARC Development Board V1.2.

2 Errata #2: HAR-12226 – CSI0_D0 and CSI0_D1 are swapped making CSI0 interface not working

Affected version:

SMARC Development Board V1.0
SMARC Development Board V1.1

Fixed in:

SMARC Development Board V1.2

2.1 Customer Impact

The CSI0 Interface is not working on SMARC Development Board V1.1.

2.2 Description

CSI0 Interface is not operational due to inverted DSI0_D0 and DSI0_D1 signal pair.

2.3 Workaround

There is no available workaround. Issue was fixed in SMARC Development Board V1.2.

3 Errata #3: HAR-12531 – USB Enable/Overcurrent flickering in over-current status

Affected version:

SMARC Development Board V1.0
SMARC Development Board V1.1

Fixed in:

SMARC Development Board V1.2

3.1 Customer Impact

The system fails to reliably detect over current events causing USB hub to not consistently shut down a faulty device or not report the fault correctly.

3.2 Description

The OC pulse generated by the protection circuit is too short, so the USB hub cannot detect the over-current condition. As a result, proper shutdown or protection does not occur.

3.3 Workaround

There is no identified workaround. The issue will be fixed in the next board revision.

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