



Verdin iMX95

HW Errata



Revision History

Document Revisions

Date	Doc. Revision	Product Version	Changes
10-Dec-2025	Rev. 0.1	V1.0	Initial Release

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1 Errata #1: HAR-12581 – CTRL_RESET_MOCl# does not stay LOW when CTRL_RESET_MICO# is LOW

Affected version:

Verdin iMX95 V1.0

Fixed in:

Verdin iMX95 V1.1

1.1 Customer Impact

Customer circuits implemented on the carrier board that rely on the CTRL_RESET_MOCl signal for delayed start-up may not operate as intended.

1.2 Description

Due to the PMIC's internal logic, the CTRL_RESET_MOCl signal is asserted high immediately after all power rails have been raised. Not waiting until the CTRL_RESET_MICO will go High.

1.3 Workaround

Will be fixed in V1.1

2 Errata #2: HAR-12557 – The CTRL_FORCE_OFF_MOCI# signal remains High after the module is powered down.

Affected version:

Verdin iMX95 V1.0

Fixed in:

Verdin iMX95 V1.1

2.1 Customer Impact

Designs utilizing the CTRL_FORCE_OFF_MOCI# signal to control system power rails are impacted. Following either a long power-button press or a software-initiated power-down request from the SoC, the CTRL_FORCE_OFF_MOCI# signal remains asserted High and does not transition Low as expected.

2.2 Description

An error in the schematic of the SoM causes the wrong behaviour of the CTRL_FORCE_OFF_MOCI# signal.

2.3 Workaround

Will be fixed in V1.1

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