Product Change Notification (PCN): 00961000 SMARC iMX95 Hexa 8GB WB IT V1.0A to 00961100 SMARC iMX95 Hexa 8GB WB IT V1.1A

Date of publication: Oct 28, 2025

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1. Affected Products

End of Life Product		Replacement Product	
Part Number	**Product Name**	**Part Number**	**Product Name**
00961000	SMARC iMX95 Hexa 8GB WB IT V1.0A	00961100	SMARC iMX95 Hexa 8GB WB IT V1.1A

2. Product Phase-in / Phase-out Schedule

End of Life Product		Replacement Product	
Part Number	**Estimated Schedule**	**Part Number**	**Estimated Schedule**
00961000	Products are available until sold out or a new version is released.	00961100	October 2025

Customers are strongly encouraged to convert their designs to the replacement parts listed above. Toradex also advises customers to carefully validate the new product version before their production release.

3. Description of Changes

Change #1: SoC part number change

Description: SoC part number has changed from NXP PIMX9596AVZXNAB to PIMX9596XVZXNAC (B0 silicon)

Customer impact:

- Hardware: No impact expected
- Embedded Linux BSP:
  - Support for SMARC iMX95 Hexa 8GB WB IT V1.0A will not be available in Embedded Linux BSP releases after BSP 7.3
  - Customers are still able to manually build a BSP image compatible with V1.0A on BSP release 7.4, however Toradex cannot guarantee future support.
  - Customers are encouraged to move their hardware to the new version (V1.1A)
- Toradex Easy Installer:
  - Support for SMARC iMX95 Hexa 8GB WB IT V1.0A will not be available in Toradex Easy Installer releases after TeZi 7.3
  - Customers are still able to manually build a TeZi image compatible with V1.0A on TeZi release 7.4, however Toradex cannot guarantee future support.
  - Customers are encouraged to move their hardware to the new version (V1.1A)
- Torizon:
  - Support for SMARC iMX95 Hexa 8GB WB IT V1.0A will not be available in Torizon releases after Torizon 7.3
  - Customers are still able to manually build a Torizon image compatible with V1.0A on Torizon release 7.4, however Toradex cannot guarantee future support.
  - Customers are encouraged to move their hardware to the new version (V1.1A)

Change #2: Fix Errata ETH reference oscillator doesn't work due to wrong supply voltage (HAR-12158)

Description: Fixed supply voltage issue on ETH reference oscillator, this allows SGMII/USXGMII/XFI interface of i.MX 95 SoC to function properly.

Customer impact:

- Hardware: SGMII/USXGMII/XFI interface will work as expected.
- Embedded Linux BSP: No impact expected
- Toradex Easy Installer: No impact expected
- Torizon: No impact expected

Change #3: Fix Errata Missing pull-up resistors on GPIO expander pins are not in line with the SMARC requirements (HAR-12173)

Description: Changed or added pull-up resistor on pins coming from GPIO expander in order to be in-line with SMARC specification

Customer impact:

- Hardware: No impact expected
- Embedded Linux BSP: No impact expected
- Toradex Easy Installer: No impact expected
- Torizon: No impact expected

Change #4: Fix Errata The DP0_HPD pin is not compliant with the SMARC specifications (HAR-12300)

Description: Pull-down resistor on DP0_HPD on pin changed to 1M Ohm in order to be in-line with SMARC specification

Customer impact:

- Hardware: Customers that replaced the 100k Pull-up resistor - R59 with a 10K resistor in the SMARC Development Carrier board V1.0 / V1.1 to get the interface to work with SMARC iMX95 V1.0 should move that R59 to its original value of 100k Ohm.
- Embedded Linux BSP: No impact expected
- Toradex Easy Installer: No impact expected
- Torizon: No impact expected

Change #5: Fix Errata Missing over current feature in the USB interfaces (HAR-12164)

Description: Footprint for USB_EN_OC# buffer/line driver was corrected. This feature will now be available.

Customer impact:

- Hardware: USB_EN_OC# will be available on all available USB interfaces
- Embedded Linux BSP: No impact expected
- Toradex Easy Installer: No impact expected
- Torizon: No impact expected

Change #6: Fix Errata ETH PHY Interrupts Not Working (HAR-12091)

Description: The CTRL_ENET_INT# signal has been disconnected from ETH_PHY1, allowing interrupts to be properly generated and used by ETH_PHY0. Consequently, ETH_PHY1 is now operated in polling mode rather than using shared interrupts. Using shared interrupts was incompatible with I2C GPIO expander utilized. To ensure correct behavior, ETH_PHY0 retains interrupt functionality, while ETH_PHY1 has been moved to a polling mechanism.

Customer impact:

- Hardware: No impact expected.
- Embedded Linux BSP: No impact is expected for systems already operating ETH_PHY1 in polling mode. Users relying on interrupt-driven handling for ETH_PHY1 must update their software accordingly.
- Toradex Easy Installer: No impact expected.
- Torizon: No impact is expected for systems already operating ETH_PHY1 in polling mode. Users relying on interrupt-driven handling for ETH_PHY1 must update their software accordingly.

Change #7: I2C_CAM0 moved from I2C4 (GPIO_IO30-31) to I2C7 (GPIO08-09) and shared with I2C_CAM1 through I2C Bus Switch (TCA9543A)

Description:

- In SMARC iMX95 V1.0, the I2C_CAM0 interface was connected to the SoC's I2C4 bus (GPIO_IO30–31). This bus was also utilized for controlling internal peripherals on the SoM.
- Starting with SMARC iMX95 V1.1, to provide users with an I²C bus free of reserved addresses caused by internal peripherals, the I2C_CAM0 and I2C_CAM1 interfaces are instead connected to the SoC's I2C7 bus (GPIO08–09) through an I²C Bus Switch (TCA9543A).

Customer impact:

- Hardware: No impact expected
- Embedded Linux BSP: No impact expected. This will be supported in BSP release 7.4
- Toradex Easy Installer: No impact expected
- Torizon: No impact expected. This will be supported in Torizon release 7.4

5. Contact

- Please contact Toradex if you have any questions.
- For commercial and sales questions, please contact shop@toradex.com
- For technical questions, please contact support@toradex.com