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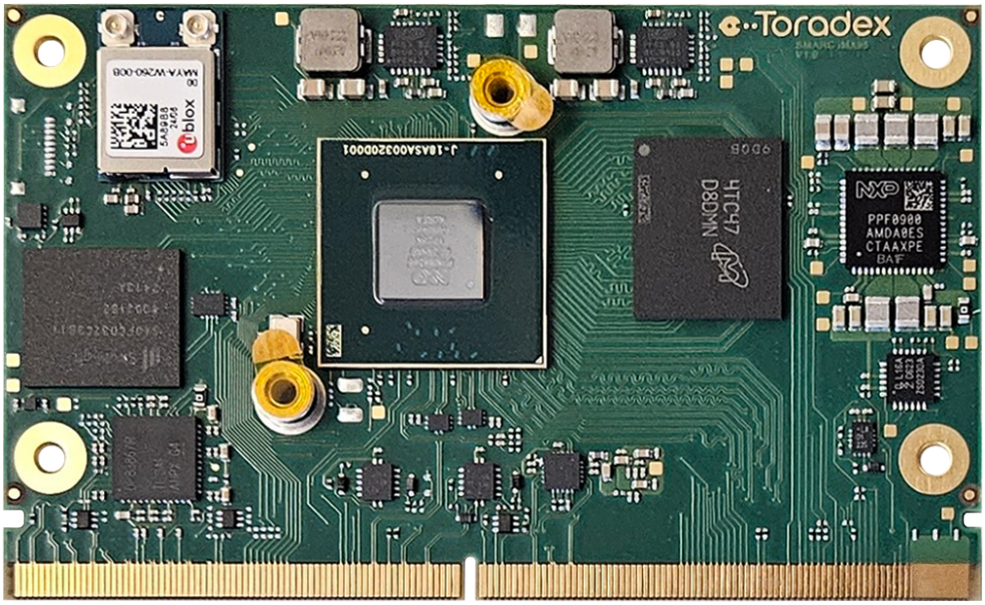
SMARC iMX95

HW Errata



Revision History

Document Revisions

Date	Doc. Revision	Product Version	Changes
21-Oct-2025	Rev. 0.1	V1.0	Initial Release

Contents

1 Errata #1: HAR-12300 – The DP0_HPD pin is not compliant with the SMARC specifications

Affected version:

SMARC iMX95 V1.0

Fixed in:

SMARC iMX95 V1.1

1.1 Customer Impact

Customers using SMARC iMX95 V1.0 with SMARC Development Board V1.0 or V1.1A will find that connected DisplayPort monitors are not detected.

1.2 Description

A design issue in the DisplayPort interface of the SMARC iMX95 V1.0 employs a pull-down resistor that is too low to provide the required signal level, when combined with SMARC Development Board V1.0 or V1.1A. In the SMARC Development Board V1.1B, the pull-up strength has been increased to mitigate the issue. The next SMARC iMX95 V1.1 will comply with the SMARC Standard.

1.3 Workaround

Customers could replace the 100k Pull-up resistor - R59 with a 10K resistor in the SMARC Development Carrier board V1.0 and V1.1 to get the interface to work with SMARC iMX95 V1.0.

2 Errata #2: HAR-12173 – Missing pull-up resistors on GPIO expander pins are not in line with the SMARC requirements

Affected version:

SMARC iMX95 V1.0

Fixed in:

SMARC iMX95 V1.1

2.1 Customer Impact

The GPIO pins without pull-up resistors might cause unpredictable behavior.

2.2 Description

There are missing pull-up resistors on GPIO pins connected to SoM GPIO Expander. Those pins are not in line with the SMARC requirements

2.3 Workaround

None

3 Errata #3: HAR-12164 – Missing over current feature in the USB interfaces

Affected version:
SMARC iMX95 V1.0

Fixed in:
SMARC iMX95 V1.1

3.1 Customer Impact

None

3.2 Description

Footprint for USB_EN_OC# buffer/line driver is wrong

3.3 Workaround

None

4 Errata #4: HAR-12568 – Boundary scan and JTAG debugging don't work if the pins are shared with the BT UART

Affected version:

SMARC iMX95 V1.0
SMARC iMX95 V1.1

Fixed in:

not scheduled

4.1 Customer Impact

JTAG debugging and Boundary Scan not working.

4.2 Description

There is a conflict on the TCK and TDI signals because they are shared with the BT UART signals. JTAG debugging and Boundary Scan only work if the R59 and R60 resistors are removed.

4.3 Workaround

None

5 Errata #5: HAR-12091 – ETH PHY Interrupts Not Working

Affected version:

SMARC iMX95 V1.0

Fixed in:

SMARC iMX95 V1.1

5.1 Customer Impact

On SMARC iMX95 V1.0, Ethernet interface ETH_PHY1 and ETH_PHY0 interrupts will not work. On SMARC iMX95 V1.1, the Ethernet interface ETH_PHY0 continues to operate normally with interrupt support while ETH_PHY1 must rely on polling mode instead of interrupts.

5.2 Description

On the SMARC iMX95 V1.0 module, both Ethernet PHYs share the interrupt signal connection to the onboard I/O expander, which prevents Ethernet interrupts from functioning correctly. In the updated SMARC iMX95 V1.1, the CTRL_ENET_INT# signal has been disconnected from ETH_PHY1, enabling interrupt-based operation for ETH_PHY0, while ETH_PHY1 now operates in polling mode.

5.3 Workaround

None

6 Errata #6: HAR-12158 – ETH reference oscillator doesn't work due to wrong supply voltage

Affected version:

SMARC iMX95 V1.0

Fixed in:

SMARC iMX95 V1.1

6.1 Customer Impact

The PCIe reference clock (Ethernet oscillator) is not working on SMARC iMX95 V1.0.

6.2 Description

The LMK6HA15625ADLER oscillator does not generate any output due to wrong supply voltage. Correct voltage is provided on the SMARC iMX95 V1.1.

6.3 Workaround

None

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