

## REVISION HISTORY

1. Design Revision V1.0 : Engineering Sample.  
Date: 14th January 2020

IF IN DOUBT ASK

Hardware Architecture  
Hardware\_Architecture.SchDoc



A

B

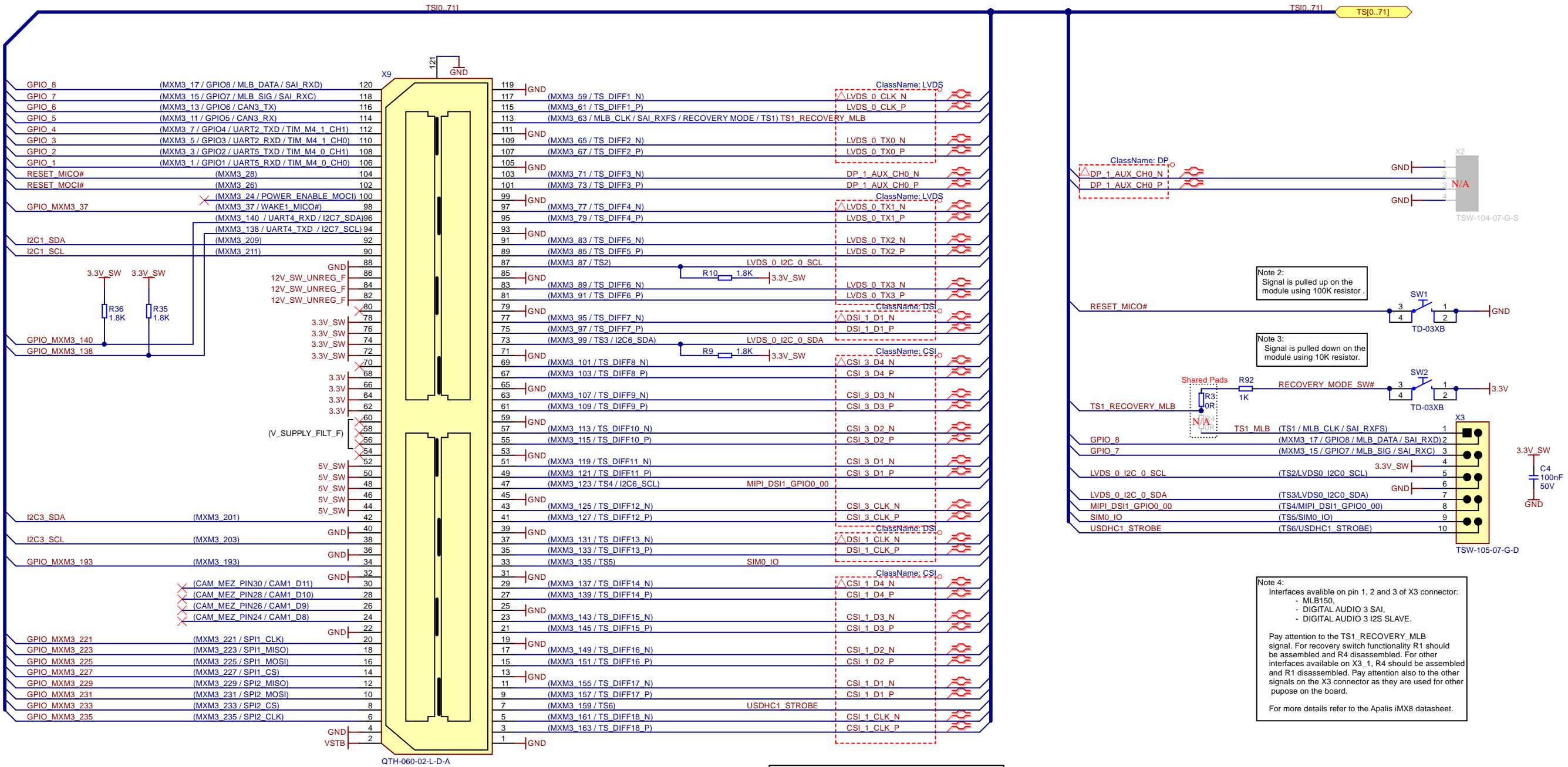
C

D



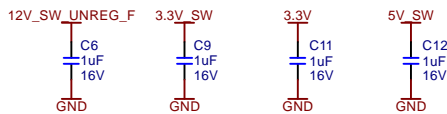
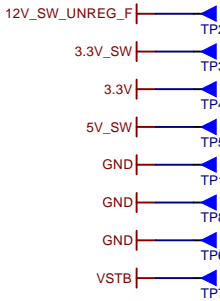
Title <i>Apalis iMX8 TS Mezzanine</i>			Toradex AG Ebenaustrasse 10 Horw 6048 Switzerland
Size: <b>A3</b>	Number: <b>1</b>	Revision: <b>V1.0</b>	
Date: <b>1/14/2020</b>	Time: <b>5:19:00 PM</b>	Sheet <b>1</b> of <b>8</b>	
File: <b>Revision_History.SchDoc</b>			





Note 1:  
On Apalis Mezzanine, track impedance used on few of the interfaces are not matched as per interface standard because the mezzanine is designed to be used with Apalis Evaluation Board for testing and evaluation purpose.

While designing custom carrier board, customers are advised to follow interface specific design standards. Please refer Apalis Carrier Board design guide for more details.



Note 2:  
Signal is pulled up on the module using 100K resistor.

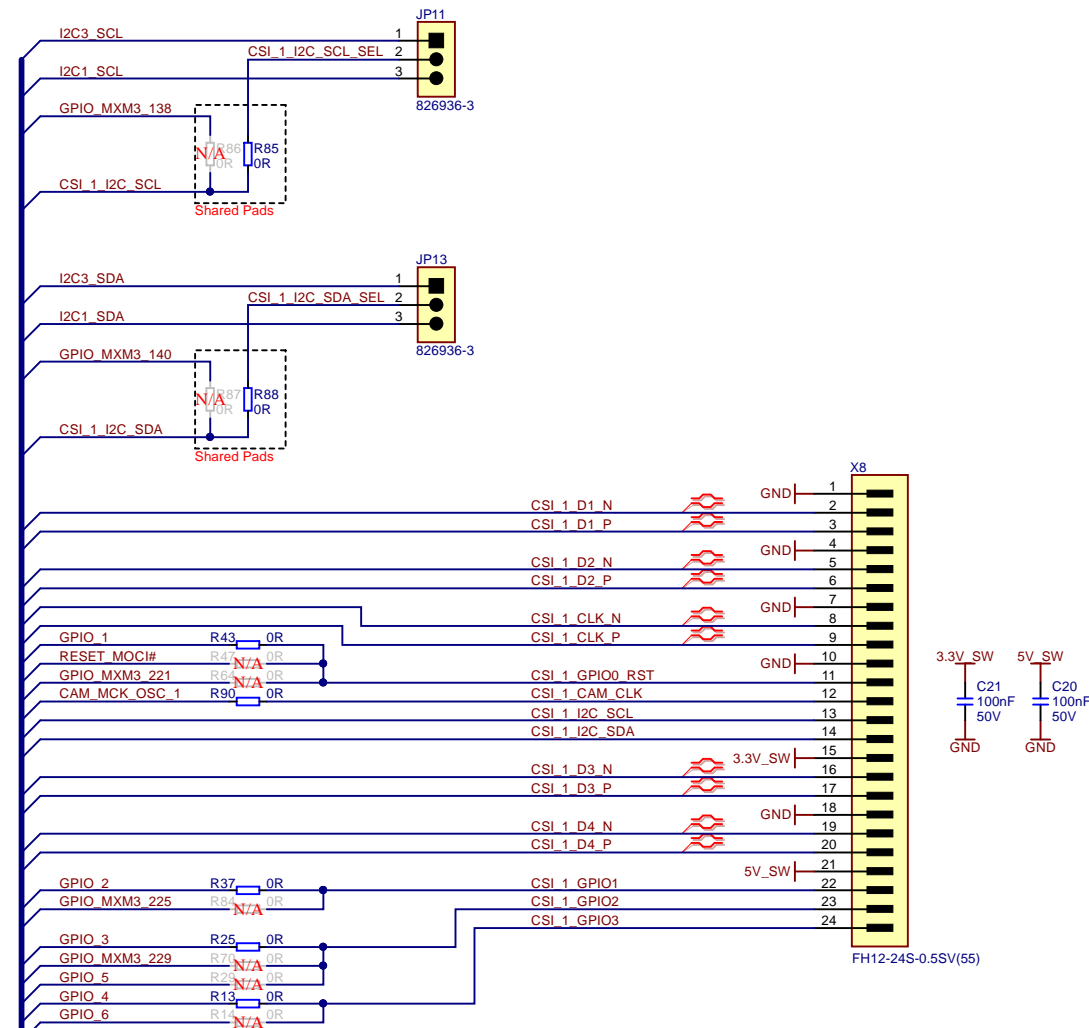
Note 3:  
Signal is pulled down on the module using 10K resistor.

Note 4:  
Interfaces available on pin 1, 2 and 3 of X3 connector:  
- MLB150,  
- DIGITAL AUDIO 3 SAI,  
- DIGITAL AUDIO 3 I2S SLAVE.

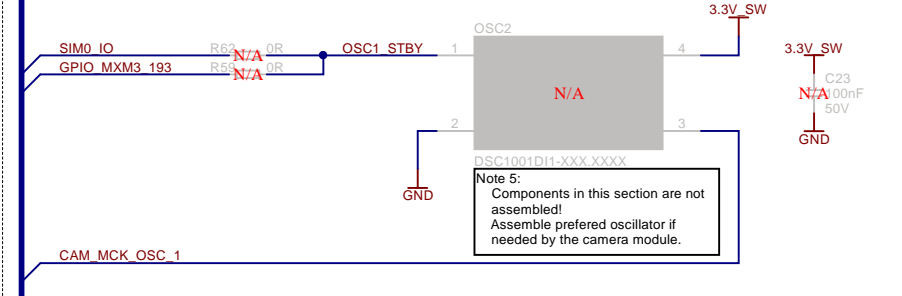
Pay attention to the TS1\_RECOVERY\_MLB signal. For recovery switch functionality R1 should be assembled and R4 disassembled. For other interfaces available on X3\_1, R4 should be assembled and R1 disassembled. Pay attention also to the other signals on the X3 connector as they are used for other purpose on the board.

For more details refer to the Apalis iMX8 datasheet.

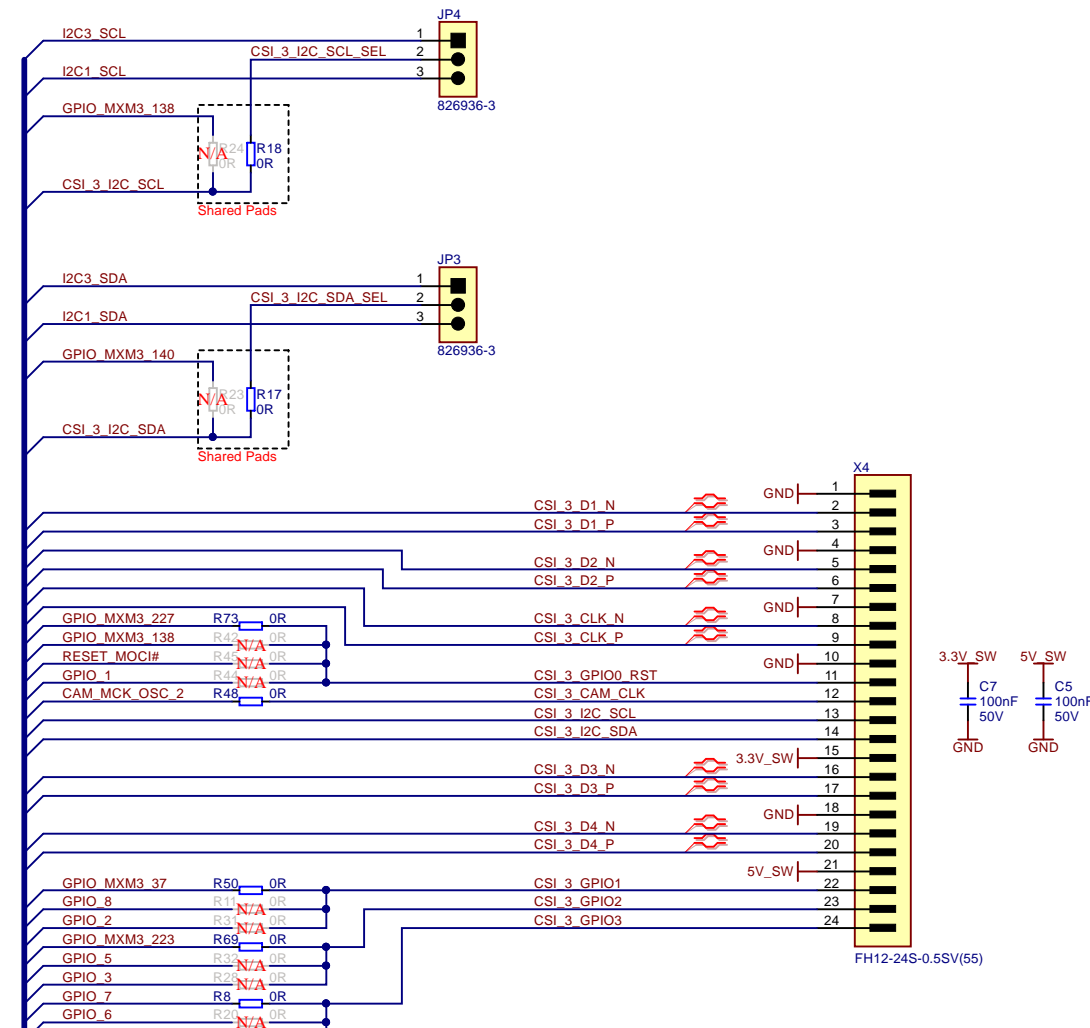
# MIPI CSI-2, CSI\_1 (QUAD LANE)



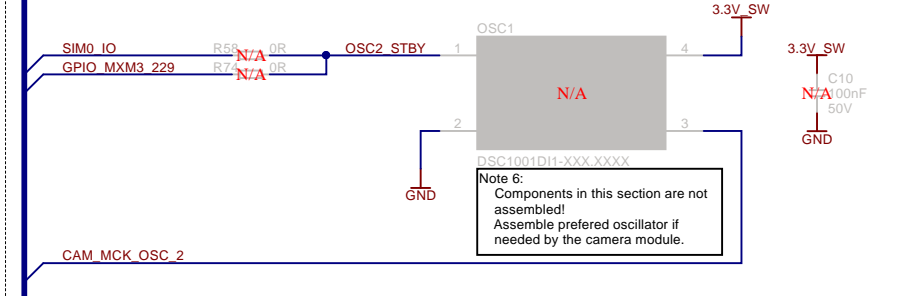
## CAM OSCILLATOR 1



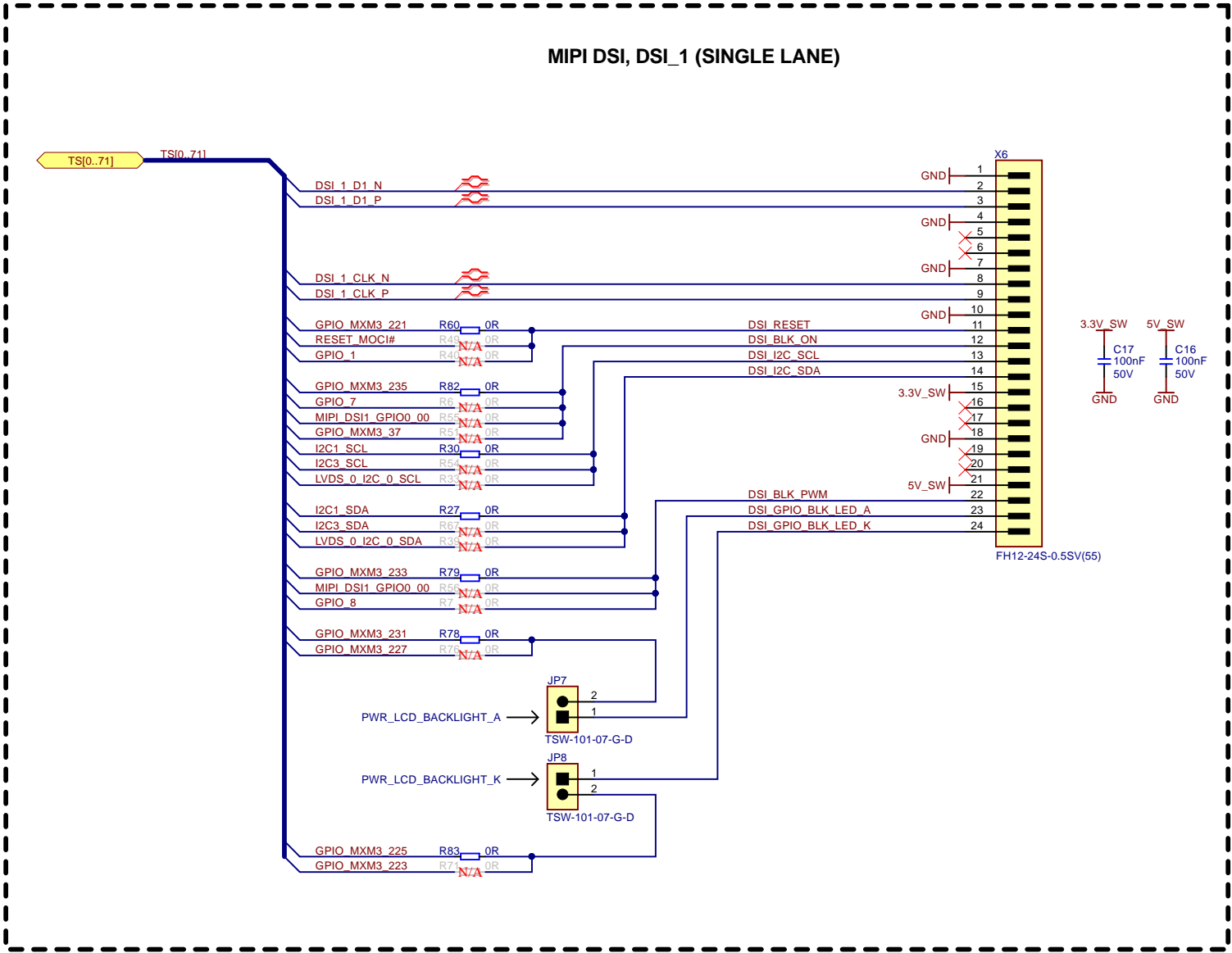
# MIPI CSI-2, CSI\_3 (QUAD LANE)



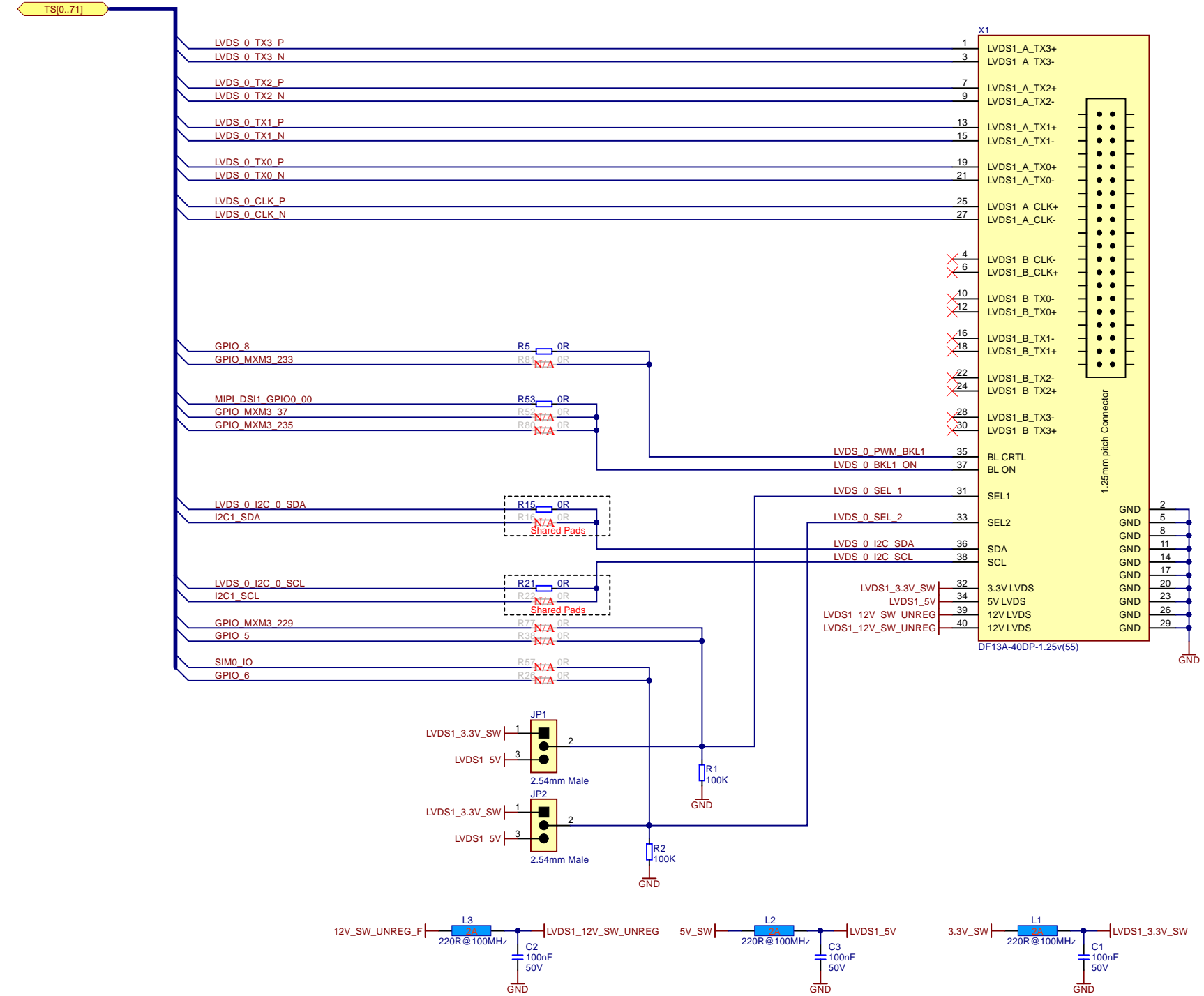
## CAM OSCILLATOR 2



Title <i>Apalis iMX8 TS Mezzanine</i>			Toradex AG Ebenaustrasse 10 Horw 6048 <i>Switzerland</i>
Size: <i>A3</i>	Number: <i>4</i>	Revision: <i>V1.0</i>	
Date: <i>1/14/2020</i>	Time: <i>5:19:00 PM</i>	Sheet <i>4</i> of <i>8</i>	
File: <i>iMX8TS Mezzanine CSI.SchDoc</i>			



LVDS, LVDS\_0 (SINGLE CHANNEL)



Title <b><i>Apalis iMX8 TS Mezzanine</i></b>			Toradex AG Ebenastrasse 10 Horw 6048 Switzerland
Size: <b>A3</b>	Number:6	Revision:V1.0	
Date: 1/14/2020	Time: 5:19:01 PM	Sheet6 of 8	
File: iMX8TS_Mezzanine_LVDS.SchDoc			

