

Product Change Notification (PCN)

Transition from Iris V1.1B to Iris V2.0A

Date of Publication: 2019-12-12

1. Toradex Product Numbers Affected

EOL Product		Replacement Product	
Part Number	Product Name	Part Number	Product Name
01351101	Iris V1.1B	01352000	Iris V2.0A

2. Product Phase in / Phase out Schedule

EOL Product		Replacement Product	
Part Number	Estimated Schedule	Part Number	Estimated Schedule
01351101	LTB (Last Time Buy): 2020-09-30 non-cancellable and non-returnable LTS (Last Time Ship): 2020-12-31	01352000	Sample Production: Limited numbers available now. Volume Production: Q2 2020

Customers are strongly encouraged to convert their designs to the replacement parts listed above. Toradex also advises customers to carefully validate the new product version before their production release.

3. Description of Changes

From 01351101 Iris V1.1B to 01352000 Iris V2.0A:

- General
 - SHIELD/CHASSIS_GND signal connection to mounting holes and connectors body (like USB, Ethernet, DVI) have been improved for better EMC behavior.
- Power Supply
 - New DC-DC buck regulators (with internal FETs) from Alpha and Omega Semiconductors have been used to increase the output current rating of the power supply. It also simplifies the power supply design and PCB layout.
 - Barrel Power connector (X6) assembly option added. Not assembled by default.
 - Fuse (F1) has been changed from 7A to 8A rated fuse to increase the input current limit.
 - Power input filter circuit has been improved.
 - Diode D13 has been changed to increase forward current rating.
- Ethernet
 - SODIMM_191 pin, components C50 and C51 are now connected to digital ground GND instead of ETH_AGND as the Ethernet circuit on the Colibri module uses digital ground GND as reference.

- RJ45 connector X15 has been changed to support the full industrial operating temperature range: -40C to +85C.
- Ethernet jumper JP2 has been removed and was replaced by resistor assembly options.
- USB
 - Micro USB Type-AB connector (X12) has been changed to SMD type connector, which helps to improve PCB layout and eliminate the PCB assembly issues.
 - Pull down resistors R30, R58, R59, R60 (15kΩ) are not assembled by default.
 - Improved ESD protection circuit.
- Micro SD
 - Use new Micro SD card holder (higher quality).
 - Pins S1 and S2 are connected to GND instead of SHIELD as these pins are required by the SD card detect switch. ESD protection diode D6 has been connected to the SD card detect pin CD1 and CD2.
 - Added load switch IC13 to control (enable/disable/power-cycle) Micro SD Card power using software. SODIMM_100 can be used control the load switch.
- Display Interface
 - New FFC connector used for Unified Interface Display X3 to support the full industrial operating temperature range.
 - Add new connector X5 for capacitive touch controller support (compatibility with Toradex display offerings).
- LVDS
 - Changed LVDS Transmitter/Serializer IC9 to support dual channel LVDS output. New LVDS Transmitter/Serializer supports both VESA and JEDIA format output data/color mapping (configurable using software OR assembly option on the carrier board). Please refer to Iris V2.0A schematics for details [1].
 - LVDS connector X7 has been changed to a 40-pin connector (compatible with the LVDS connector on the Ixora and Apalis Evaluation Board).
- Audio
 - Microphone input signal connected to the Audio connector X6. Added biasing circuit to the microphone input.
 - By default, Audio connector X6 will follow CTIA (AHJ) pinout standard which is newer and widely used pinout. Assembly option has been added to keep compatibility with OMTP pinout standard (used on Iris V1.1).
- RTC:
 - Changed the RTC battery series resistor from 22Ω to 1kΩ for current limitation purpose.
- Battery holder
 - Improve battery holder BAT1 footprint

For further details, please refer to the public design data (Altium Project, Schematics) of Iris V1.1B and V2.0A:

[1] <https://developer.toradex.com/products/iris-carrier-board#design-resources>

4. Customer Impact

4.1. Hardware Design

- New LVDS connector X7 is not backward compatible.
- SHIELD / CHASSIS_GND changes may have an impact on EMI behavior.
- For customers using Colibri PXAxxx, R30, R58, R59, R60 (15kΩ) need to be assembled for pull-down on USB.
- For customers using PXA270, assembly option for the Ethernet needs to be changed. When using Colibri PXA270 with Iris V2.0A, assemble resistor R121 and remove resistors R122 and R123. Please refer to Iris V2.0A schematics for details [1].

4.2. Software

- New LVDS Transmitter/Serializer uses GPIOs for configuration, which can be changed in software. Please refer to Iris V2.0A schematics for details [1].
- Toradex has tested and validated the following BSPs for Iris V2.0A:
 - Linux: BSP V3.0b2 and higher
 - Windows CE:
 - Colibri VFxx: V1.5b4 and higher
 - Colibri T20/T30: V2.0b4 and higher
 - Colibri iMX6: V1.0b6 and higher
 - Colibri iMX7: V0.1b2 and higher

5. Definitions

LTB: Last Time Buy

LTS: Last Time Ship

EOL: End Of Life

6. Contact

Please contact Toradex if you have any questions.

For commercial and sales questions please contact shop@toradex.com

For technical questions please contact support@toradex.com