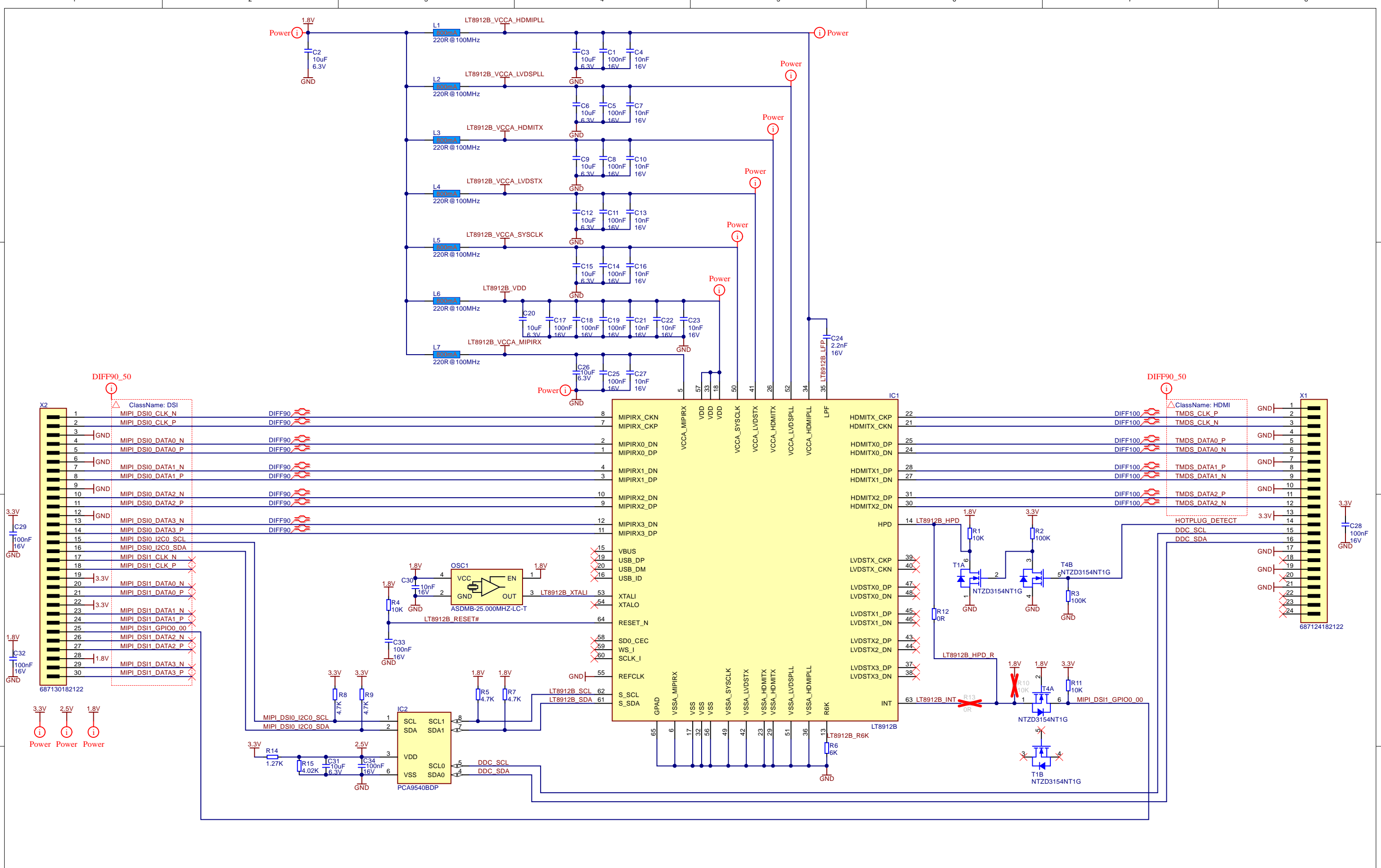


1	2	3	4	5	6	7	8
<div>REVISION HISTORY</div> <div>IF IN DOUBT ASK</div> <div>1. Design Revision V1.0 : Initial design. Date: 18th Oct 2018</div> <div>2. Design Revision V1.1 : I2C architecture modified by adding the device NXP PCA9540BDP. It will allow the bus to be used for DDC or IC1 configuration. Date: 25th March 2019</div>							



Title <i>DSI to HDMI Adapter</i>			Toradex AG Altsagenstrasse 5 Horw 6048 <i>Switzerland</i>
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Mechanicals

Label: "DSI to HDMI Adapter XXXXXXXX"

PCB DSI to HDMI Adapter V1.1.001


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