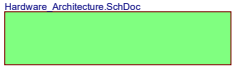
REVISION HISTORY

1. Design Revision V1.0 : Preliminary design.
   Date: 10th Dec 2014

2. Design Revision V2.0 :
   - iMX6TS_Mezzanine.SchDoc: Updated connection from mezzanine connector X1
   - iMX6TS_Mezzanine_CSI.SchDoc: Added new MIPI CSI connector X4
   - iMX6TS_Mezzanine_DSI.SchDoc: Added new MIPI DSI connector X2
   Date: 03rd Oct 2017

IF IN DOUBT ASK

Hardware Architecture
Hardware_Architecture.SchDoc



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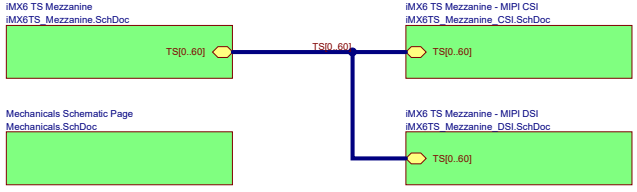
Title *Apalis iMX6 TS Mezzanine*			*Toradex AG* *Altsagenstrasse 5* *Horw* *6048* *Switzerland*
Size: A3	Number:1.	Revision:V2.0	
Date: 23-03-2018 Time: 14:56:31		Sheet 1 of 6	
File: Revision_History.SchDoc			



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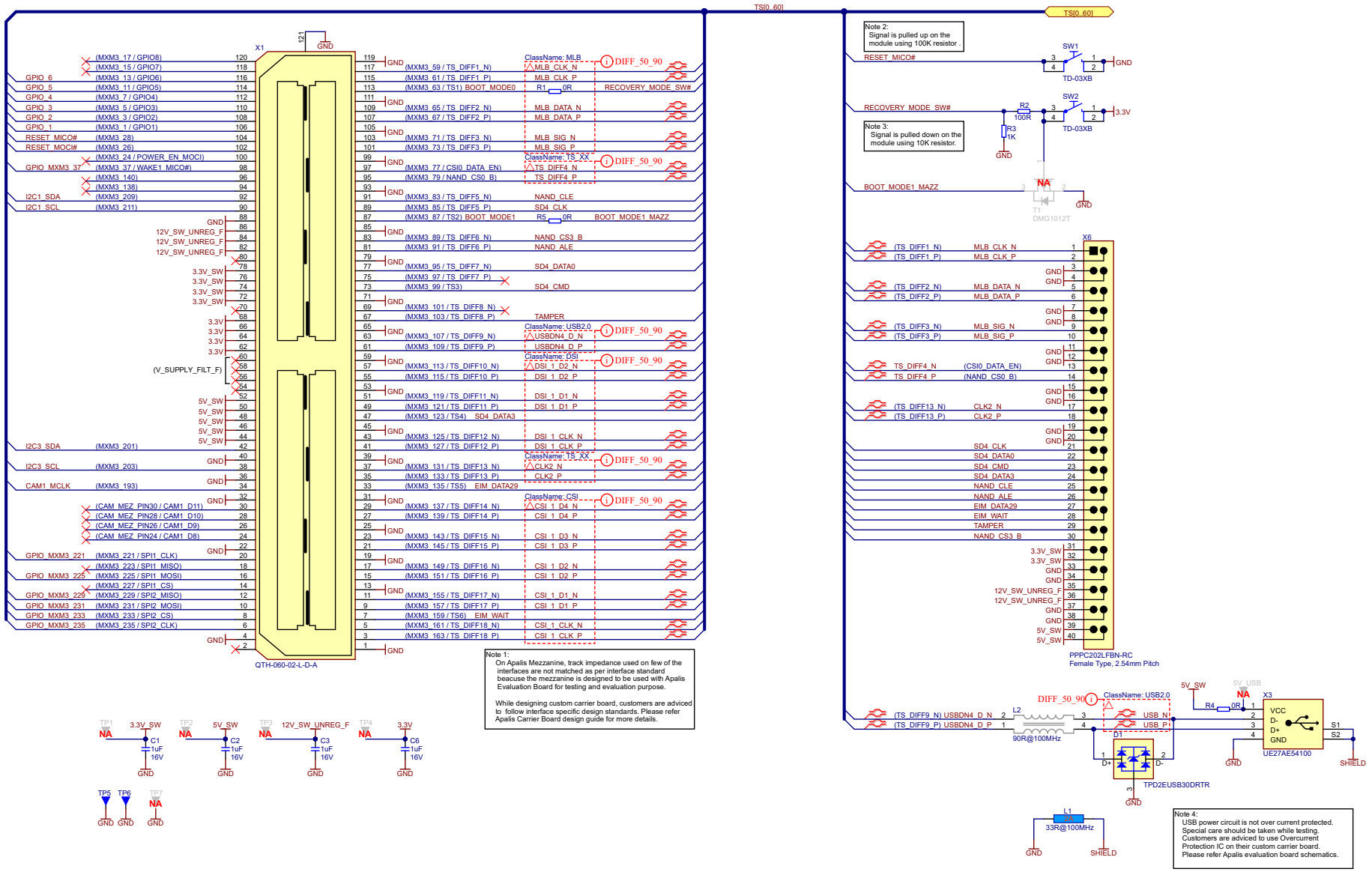
Title *Apalis iMX6 TS Mezzanine*			*Toradex AG* *Altsagenstrasse 5* *Horw* *6048* *Switzerland*
Size: A3	Number:2.	Revision:V2.0	
Date: 23-03-2018 Time: 14:56:31		Sheet 2 of 6	
File: Hardware Architecture.SchDoc			



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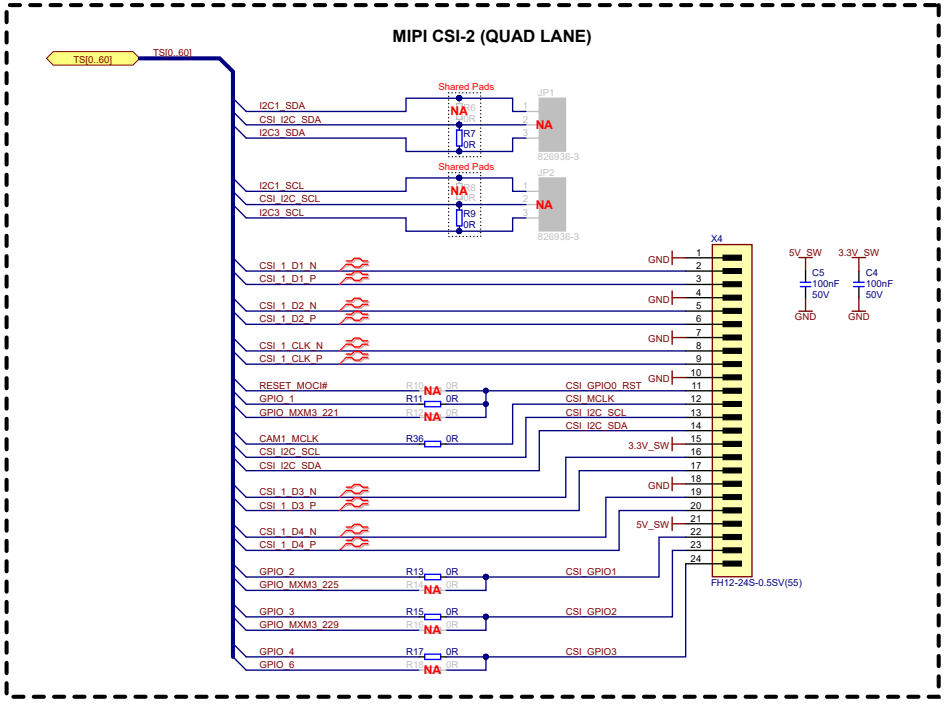
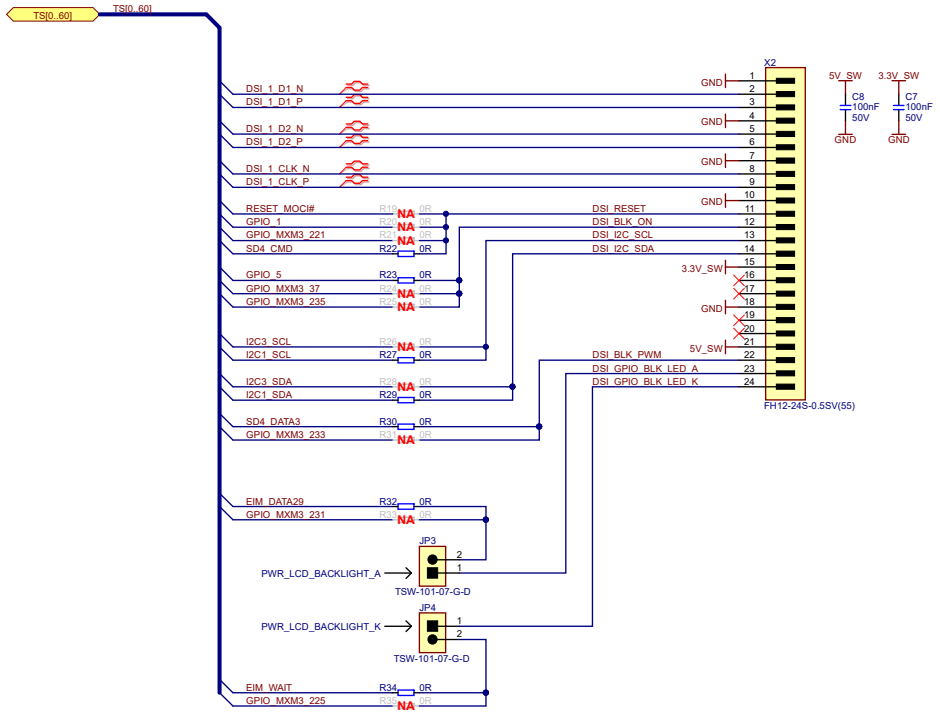
Title: *Apalis iMX6 TS Mezzanine*		*Toradex AG Altsagenstrasse 5 Horw 6048 Switzerland*
Size: A3	Number:3.	Revision:V2.0
Date: 23-03-2018 Time: 14:56:31		Sheet 3 of 6
File: iMX6TS_Mezzanine.SchDoc		

MIPI CSI-2 (QUAD LANE)



Title	*Apalis iMX6 TS Mezzanine*		*Toradex AG Altsagenstrasse 5 Horw 6048 Switzerland*
Size: A3	Number:4.	Revision:V2.0	
Date: 23-03-2018 Time: 14:56:31		Sheet4 of 6	
File: iMX6TS_Mezzanine_CSI.SchDoc			

MIPI DSI (DUAL LANE)



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Title *Apalis iMX6 TS Mezzanine*			*Toradex AG Altsagenstrasse 5 Horw 6048 Switzerland*
Size: A3	Number:5.	Revision:V2.0	
Date: 23-03-2018 Time: 14:56:32		Sheet 5 of 6	
File: iMX6TS_Mezzanine_DSI.SchDoc			

Toradex Logo
MECH1

Fiducial
MECH10

Fiducial
MECH3

Fiducial
MECH4

Fiducial
MECH5

Fiducial
MECH6

Fiducial
MECH7

Fiducial
MECH8

Fiducial
MECH9

PCB Apalis iMX6 Mezzanine V2.0
PCB1

Label: "Apalis iMX6 Mezzanine V2.0A"
LBL1

S1002-OPS60-01G
NA
SJP1

S1002-OPS60-01G
NA
SJP2

S1002-OPS60-01G
SJP3

S1002-OPS60-01G
SJP4

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Title *Apalis iMX6 TS Mezzanine*			*Toradex AG* *Altsagenstrasse 5* *Horw* *6048* *Switzerland*
Size: A3	Number:6.	Revision:V2.0	
Date: 23-03-2018 Time: 14:56:32		Sheet6 of 6	
File: Mechanicals.SchDoc			