

REVISION HISTORY

IF IN DOUBT ASK

23 August 2012

0. Initial Release

18 September 2012

- 1. New Hardware Revision, Colibri Evaluation Baord V3.1
- 2. Unified Interface schematic page
 - Swapped the designators of the resistors R89 and R90.
 - Swapped the designators of the resistors R100 and R101.

28 September 2013

- 3. Schematic library and SD Card schematic page
 - SD card image has been updated in the schematic library. SD card connector on the schematic page has been updated to reflect the library changes.

21 November 2013

- 4. USB schematic page
 - The NOTE 1 has been added in the schematic page: "It is recommended to assemble 0R resistor instead of 22R for R102 and R103".

04 September 2014

- 5. All schematic pages
 - Schematic page template has been updated.
 - "Port Cross Reference" has been added to the project.
 - Text comments for not assembled components (NA) have been removed.
 - Schematic page numbering has been updated .
- 6. Revision history schematic page
 - Revision history page has been added to the project.
- 7. Ethernet schematic pages
 - The NOTE 2 has been added to the schematic page: "It is recommended to connect SODIMM 191 (ETH_GND) pin directly to the GND".



Title Evaluation Board 3.1			Toradex AG Altsagenstrasse 5	
Size: A3	Number: 1.	Revision: V3.1		Horw 6048
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File: RevisionHistory.SchDoc				

Revision History
Revision History.SchDoc

Audio
Audio.SCHDOC

Ethernet
Ethernet.SCHDOC

SDCard
SDCard.SCHDOC

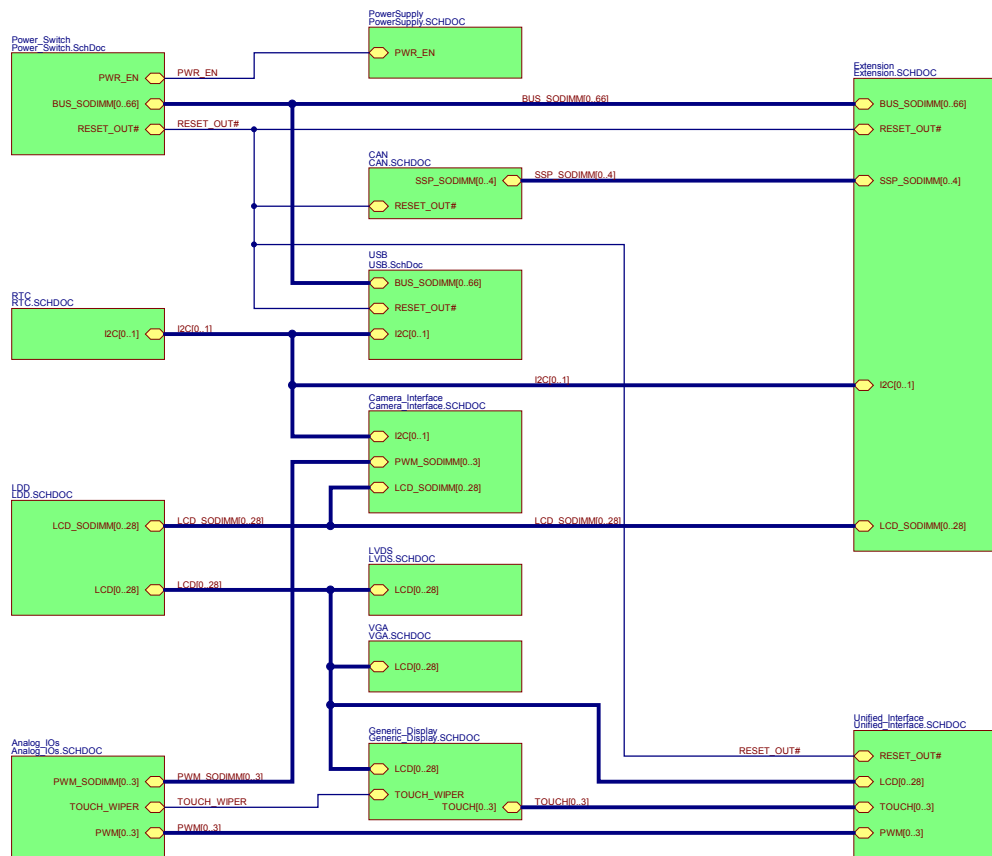
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GPIOs.SCHDOC

Serial
Serial.SCHDOC

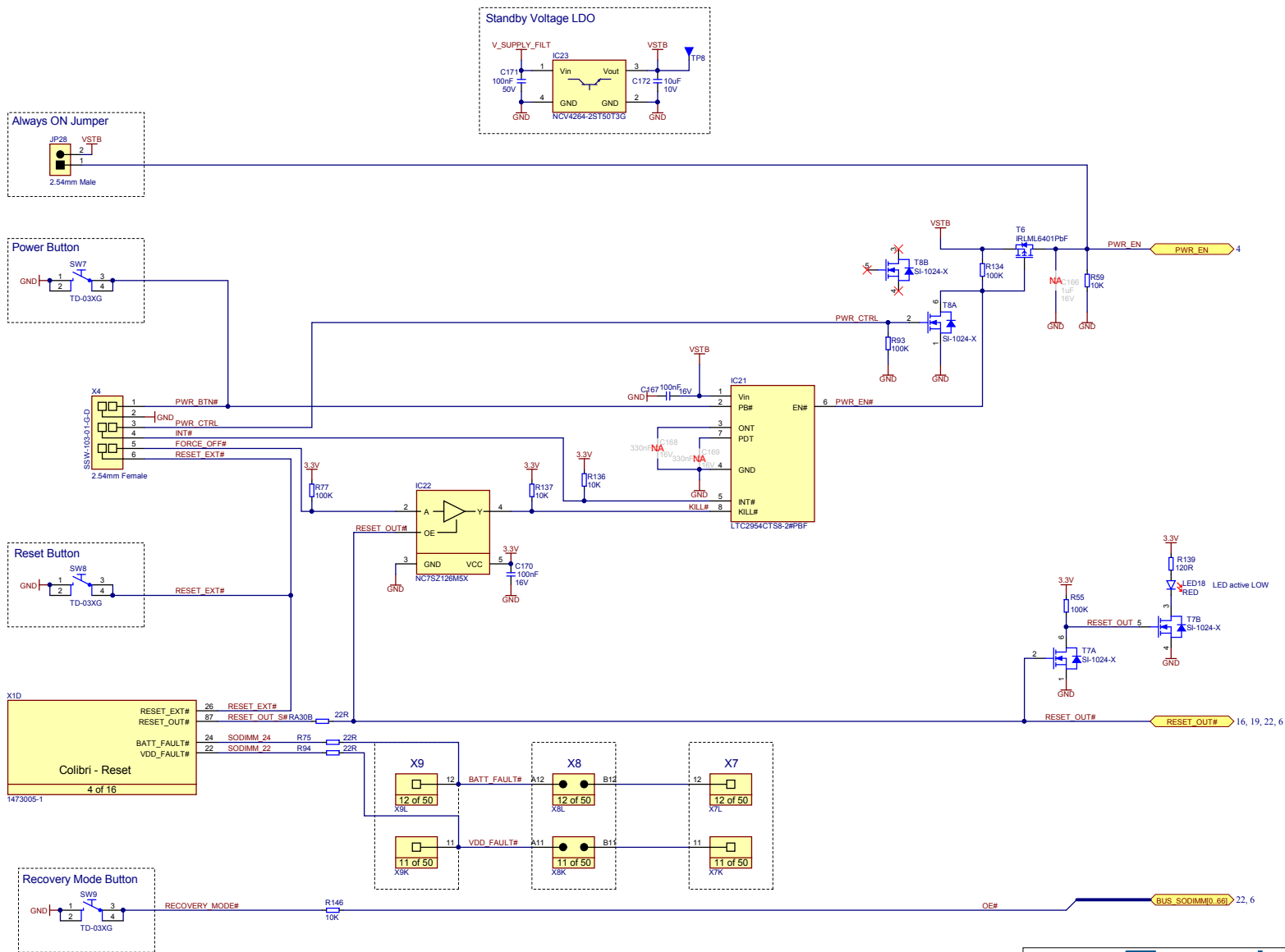
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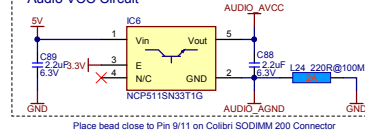
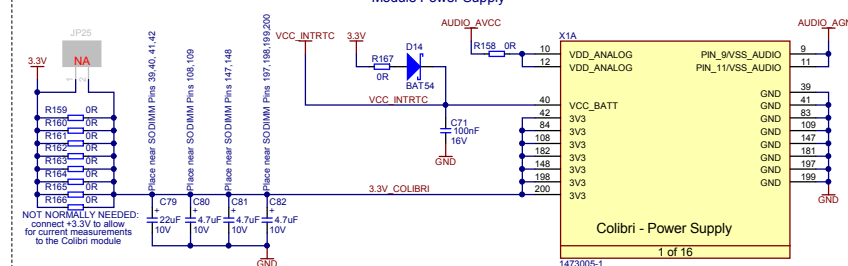
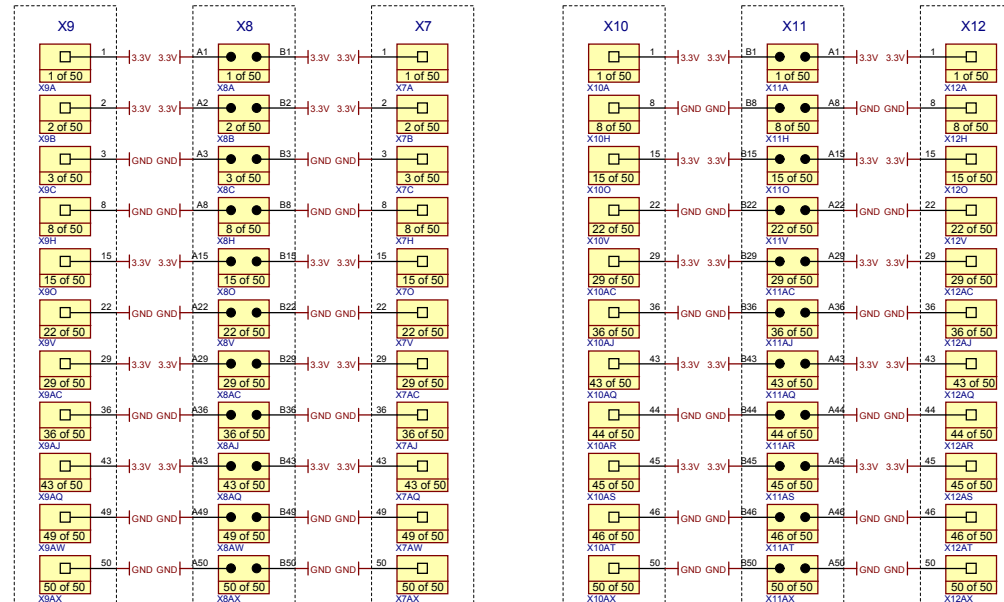
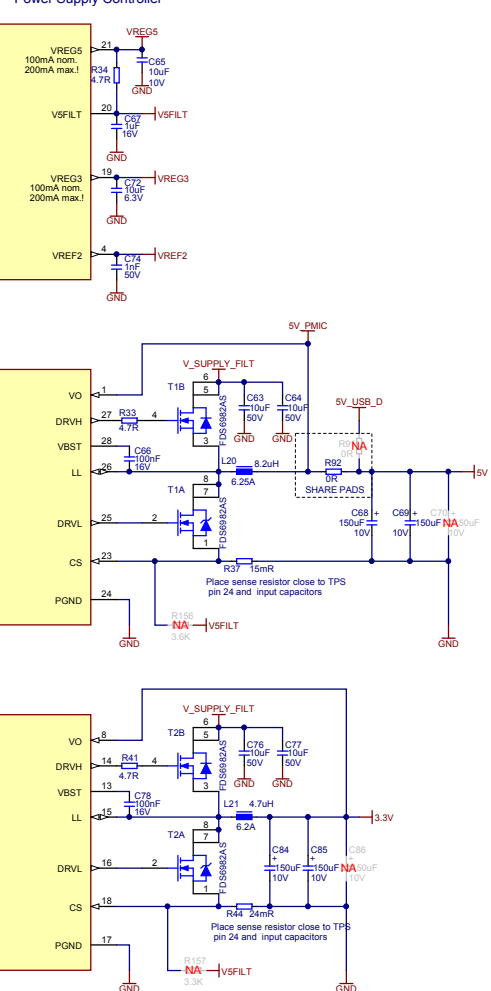
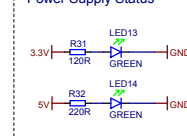
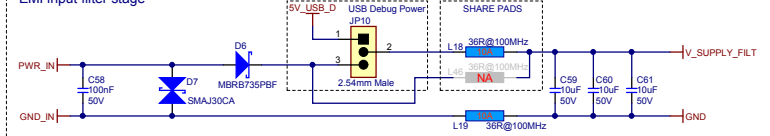
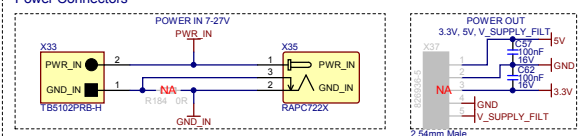
LEDs_Switches
LEDs_Switches.SCHDOC

DVI
DVI.SCHDOC



Title Evaluation Board 3.1		Toradex AG Altsagenstrasse 5 Horn 6048 Switzerland	
Size: A3	Number: 2.	Revision: V3.1	
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File: Hardware Architecture.SchDoc			





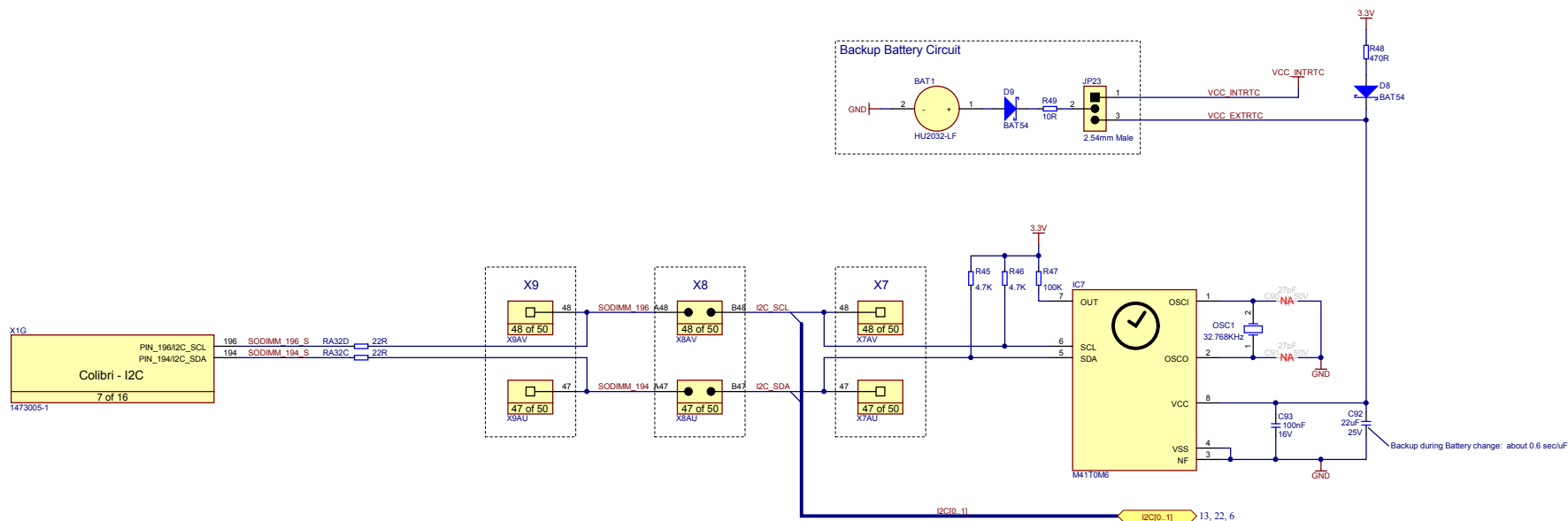
Title	<i>Evaluation Board 3.1</i>
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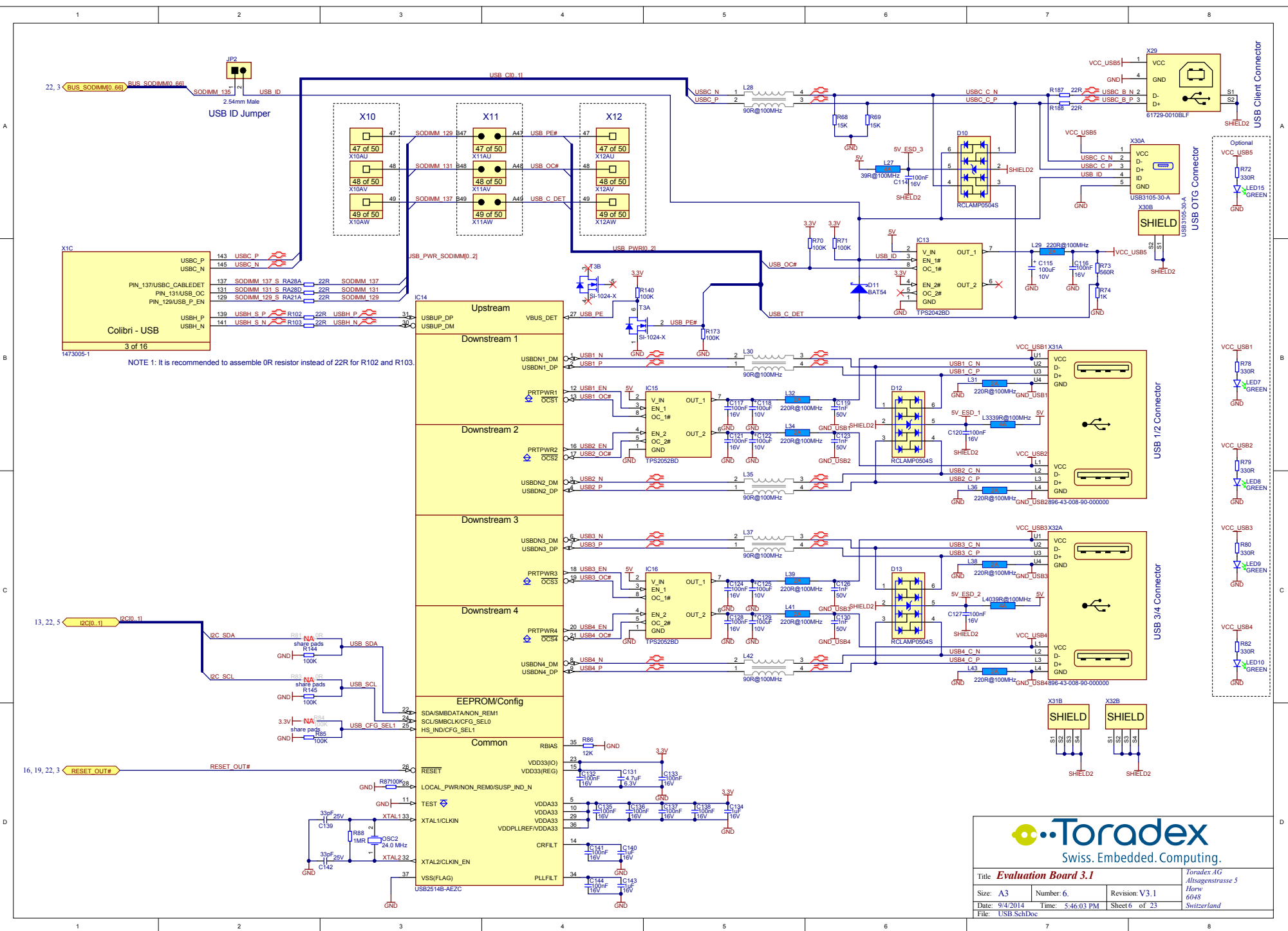
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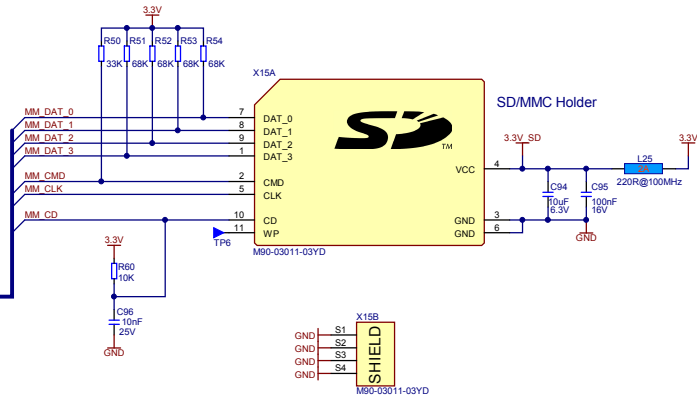
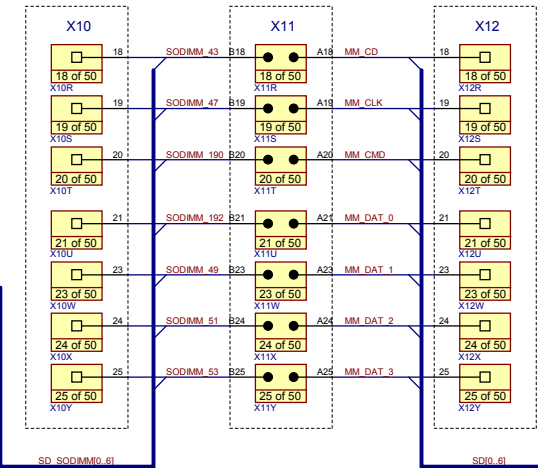
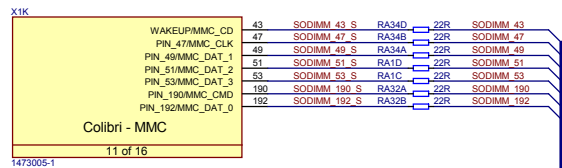
Date: 9/4/2014 Time: 5:46:02 PM Sheet 4 of 23

Date:	7/4/2014	Time:
File:	PowerSupply.SchDoc	

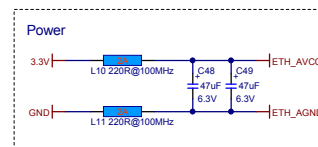
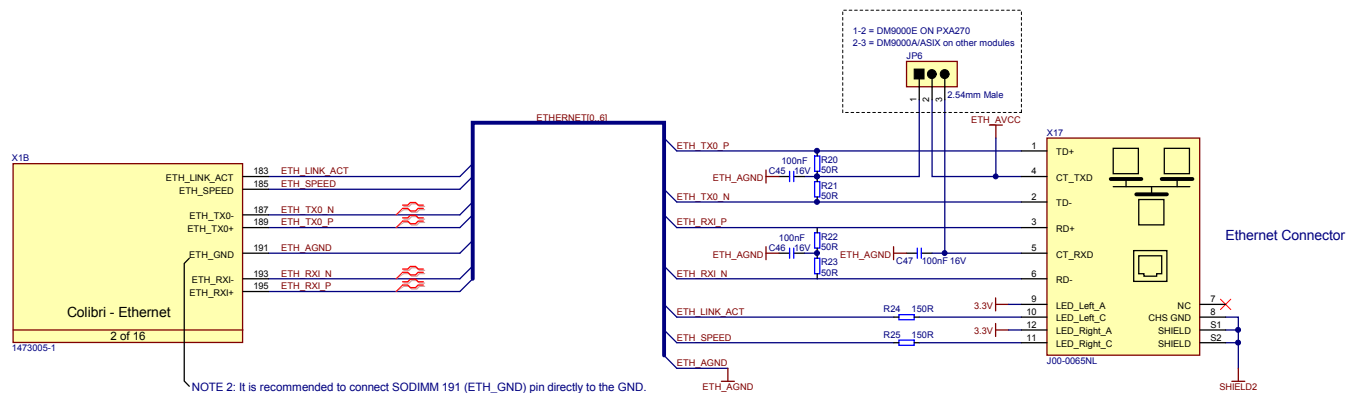
Toradex AG
Altsagenstrasse 5
Horw
6048
Switzerland

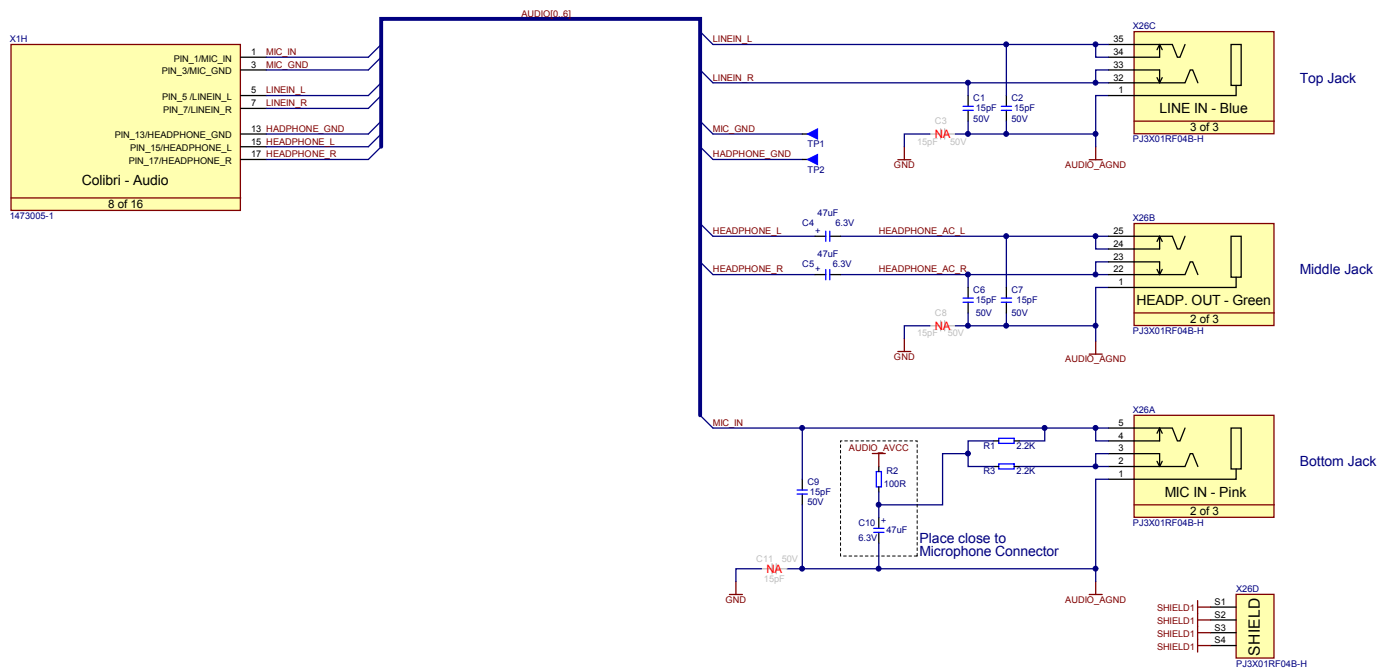


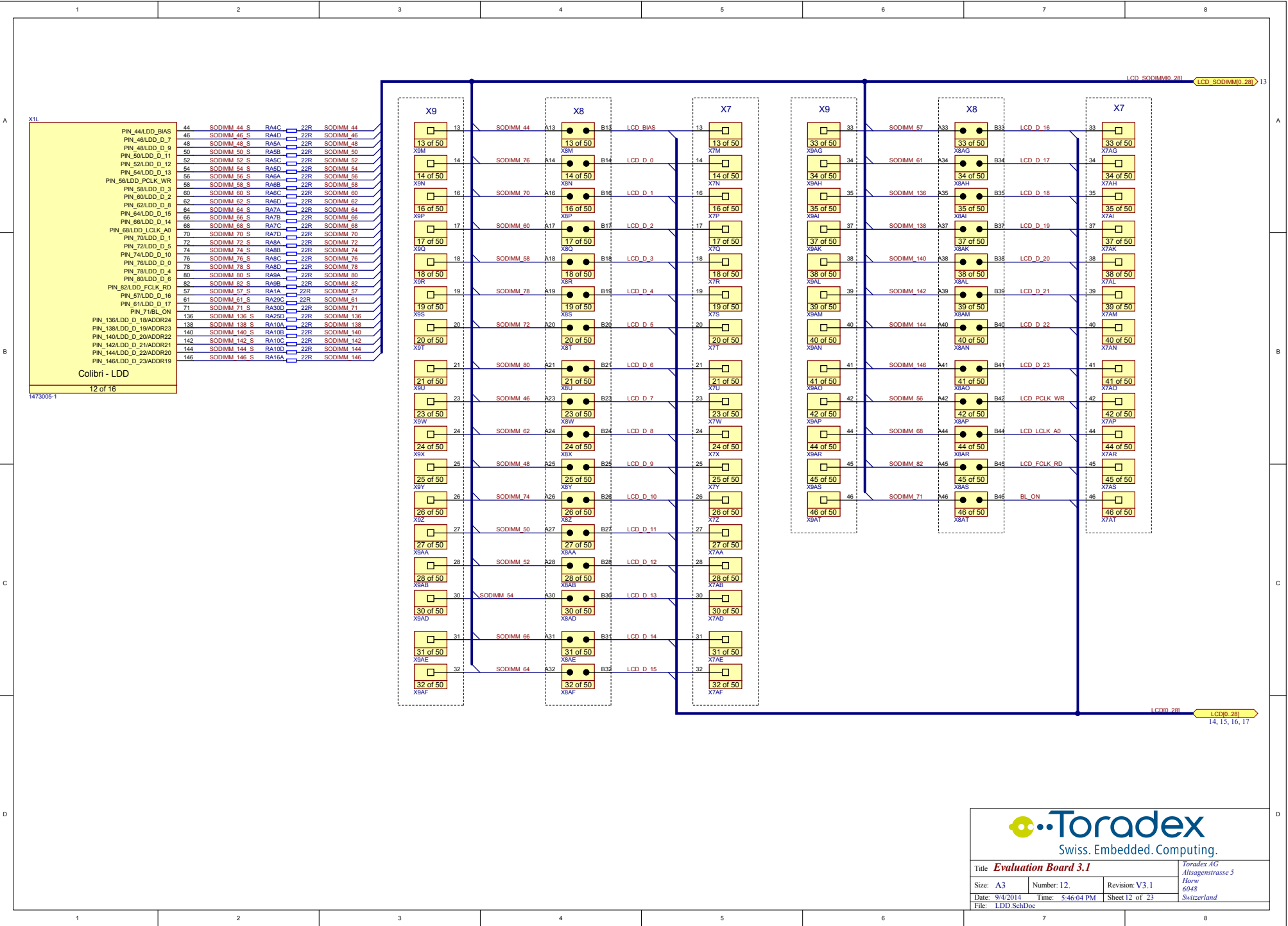




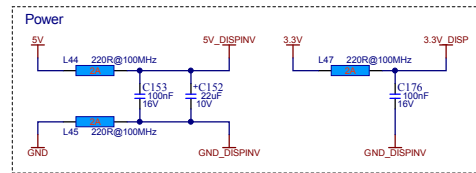
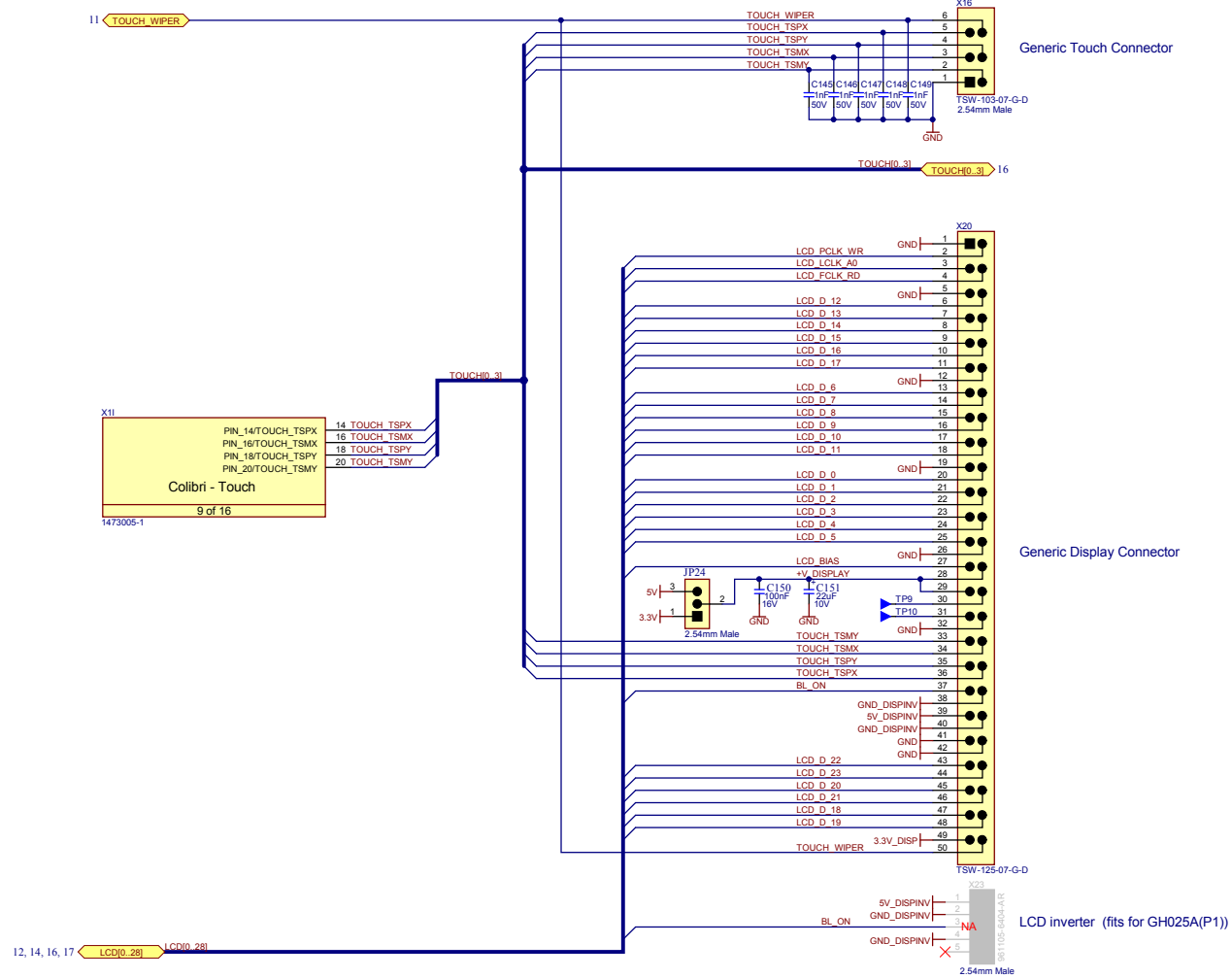
Title Evaluation Board 3.1				Toradex AG Altsagenstrasse 5	
Size: A3	Number: 7.	Revision: V3.1		How 6048	
Date: 9/4/2014	Time: 5:46:03 PM	Sheet 7	of 23	Switzerland	
File: SDCard.SchDoc					

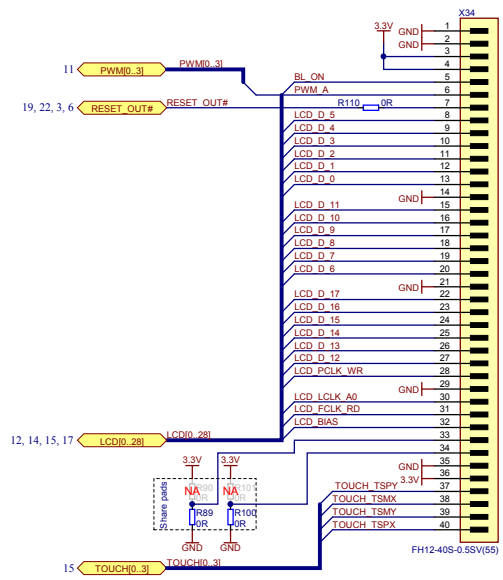






Title Evaluation Board 3.1			Toradex AG Altsagenstrasse 5 Horn 6048 Switzerland
Size: A3	Number: 12.	Revision: V3.1	
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Unified Interface Display Connector



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Size: A3	Number: 16.	Revision: V3.1	Toradex AG Altsagenstrasse 5 Horn 6048 Switzerland
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File: Unified Interface.SchDoc			

