

EDT 7.0" TFT WVGA With Touch V1.0a







Revision History

Date	Doc. Rev.	EDT 7.0" TFT WVGA with Touch Version	Changes
12-Mar-12	Rev. 1.0	V1.0a	Preliminary Release
17-Aug-17	Rev. 1.1	V1.0a	Removed document meta-data



EXAMINED BY:		FILE NO . CAS-0006972
Vincent Uh	EMERGING DISPLAY	ISSUE : APR.21, 2011
APPROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 30
David Chang		VERSION: 4
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
	DEL NO.: ET070080DH6 (RoHS) MESSRS:	



EMERG	ING D	ISPLAY	MOD	EL NO.					VE	ERSION	PAGE
	OGIES CORI		ET070080DH6						4	0-1	
			DOC	. FIRS	T IS:	SUE					
RECORD	S OF R	EVISION								5	SEP.24, 200
DATE	REVISED PAGE NO.		SUMMARY								
SEP.30, 2009	3	VDD-VSS : 3.0(LECTRICAL CHARACTERISTICS DD-VSS: 3.0(MIN.) → 3.2(MIN.) DD NOTE(4)								
	12,13	10. INTERFACE S NO.30~32 : AI			· MC	DE S	FTTIN	ſĢ.			
	14	11.1 POWER SUP ADD MINIM MODIFY 11.2 TH CUI	PLY FO UM 1K E BRIC RRENT	OR LCM THz FRE HTNES OF LEI	I QUE SS CO DCTI	NCY ONTRO RL.	OLLEI	O BY I	BACK	LIGHT	
JAN.21, 2010	2	3.1 ELECTRICAL							DE	A CA DIZ	•
		PARAMETER LED BACKLIGHT PO' DISSIPATION LED BACKLIGHT FOI	WER	SYMBOL PD	MIN		IAX. :590	UNIT mW	RE	MARK	
		CURRENT	KWAKD	IF			800	mA			
		PARAMETER LED BACKLIGHT PO		SYMBOL PD	MIN		IAX.	UNIT	RE	MARK	1
		DISSIPATION LED BACKLIGHT FOI CURRENT	RWARD	IF.		_	240	mA			
	3	4. ELECTRICAL (CHAR/	ACTERI	STIC	S					J
		PARAMETER POWER SUPPLY	SYME	VDD	ITION -VSS	MIN.	TYP. (220)	MAX. (270)	UNIT mA	REMARK NOTE (2)	
		CURRENT POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC V	.3V SS=3.3V /L=ON	8 <u>8</u>	(600)	(780)	mA	1	
		LED LIFE TIME		- -		(30K)	(40K)	-	HRS		
		PARAMETER POWER SUPPLY	SYME	VDD	-VSS	MIN.	TYP. 220	MAX. 270	UNIT mA	REMARK NOTE (2)	
		CURRENT POWER SUPPLY CURRENT FOR LED	ICC	= 3	.3V SS=3.3V /L=ON		600	780	mA		
		DRIVER LED LIFE TIME	12.1		0	30K	40K	0.2	HRS		
	7	6.1 OPTICAL CHA	ARACI	TERISTI	CS						
		I T E M	SYMBOL θ _{p+}	CONDITION θ_x =0°	MIN. (60)	(65)	AX. UNII	REMARK	7		
		VIEWING ANGLE CONTRAST RATIO	θ_{x} θ_{x} CR	$CR \ge 10$ $\theta_y=0^\circ$ $\theta x=0^\circ$, $\theta y=0^\circ$	(60) (65) (65) (300)	(65) (70) (70)	deg 	NOTE (2) NOTE (3)			
		COLOR OF RED	Wx Wy Rx	θx=0°, θy=0°	(0.26) (0.29) (0.56)	(0.34) (0.36) -	NOIE (3)	1		
		CIE COORDINATE GREEN BLUE	RY Gx Gy Bx	IF = 160 mA (NTSC : 45 %)	(0.30) (0.26) (0.49) (0.09)	(0.31) ((0.54) ((0.14) (0.40) — 0.36) — 0.59) —	NOTE (5)			
		THE BRIGHTNESS OF MODULE	By B	θx=0°, θy=0° IF = 160mA	(250)	(0.16) (0.21) — cd/m	NOTE (6)			
		I T E M	$\theta_{y^{\pm}}$	CONDITION θ_x =0°	60	65	AX. UNII	REMARK	7		
		VIEWING ANGLE CONTRAST RATIO	θ_{x_i}	$CR \ge 10$ $\theta_y=0^\circ$ $\theta x=0^\circ$, $\theta y=0^\circ$	60 65 65 300	65 70 70 350	deg	NOTE (2) NOTE (3)			
		COLOR OF RED CIE COORDINATE GREEN	Wx Wy Rx RY Gx Gy	θx=0°, θy=0° IF = 160 mA (NTSC : 45 %)	0.26 0.29 0.51 0.29 0.28 0.53	0.31 0.34 0.56 0.34 0.33 0.58	0.36 0.39 0.61 0.39 0.63	NOTE (5)			
		THE BRIGHTNESS OF MODULE	Bx By B	θx=0°, θy=0° IF = 160mA	0.10 0.07 250		0.20 0.17 — cd/m	NOTE (6)	-		
APR.21, 2011	2	3.1 ELECTRICAL VDD-VSS: (M		LUTE N			RATI	NGS	_		
	8	NOTE (2): $\theta y \rightarrow \theta$		1 → (IVI)	M.)	J.J					
		NOTE (4) : TR AN	-	$\rightarrow T_R AN$	D T _F	18					



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1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO :

HIMAX HX8262-A HIMAX HX8678-A

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

2. MECHANICAL SPECIFICATIONS

(1) DISPLAY SIZE	 7 inch
(2) NUMBER OF DOTS	 800W * (RGB) * 480H DOTS
(3) MODULE SIZE	 166W * 105.44H *10.95D(MAX.) mm
	(WITHOUT FPC & LED BL'S CABLE)
(4) EFFECTIVE AREA	 155.6W * 94.6H mm (T/P)
(5) ACTIVE AREA	 152.4W * 91.44H mm (LCD)
	154W * 93.04H mm (T/P)
(6) DOT SIZE	 0.0635W * 0.1905H mm
(7) PIXEL SIZE	 0.1905W * 0.1905H mm
(8) LCD TYPE	 \ensuremath{TFT} , $\ensuremath{TRANSMISSIVE}\xspace$, $\ensuremath{ANTE-GLARE}\xspace$
(9) COLOR	 262K
(10) VIEWING DIRECTION	 6 O'CLOCK
(11) BACK LIGHT	 LED, COLOR: WHITE
(12) INTERFACE MODE	 RGB 18BIT PARALLEL
	(DE/SYNC MODE)



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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.5	6.5	V	
INPUT VOLTAGE	VIN-VSS	- 0.3	VDD+0.3	V	
STATIC ELECTRICITY	_	_		V	NOTE(1)
LED BACKLIGHT POWER DISSIPATION	PD	_	2592	mW	
LED BACKLIGHT FORWARD CURRENT	IF	<u></u> 3	240	mA	

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPER.	ATING	STOR	RAGE	REMARK	
TIEM	MIN. MAX.		MIN. MAX.		KEWAKK	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE(1),(2)	
HUMIDITY	NOTI	7(2)	NOT	7(2)	WITHOUT	
HUMDITI	NOT	3(3)	(3) NOTE(3)		CONDENSATION	
VIBRATION	Ţ	2.45 m/s ² (0.25 G)	ĺ	11.76 m/s ² (1.2 G)	5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X,Y,Z,TOTAL 3HRS	
SHOCK	_	29.4 m/s ² (3 G)	_	490 m/s ² (50 G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH	
CORROSI VE GAS	NOT ACC	EPTABLE	NOT ACC	EPTABLE		

NOTE (1): Ta AT -30°C: 48HRS MAX.

80°C: 168HRS MAX.
NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): Ta < 60°C: 90%RH MAX (96HRS MAX).

 $T_a \! > \! 60^{\circ}\text{C}$: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).



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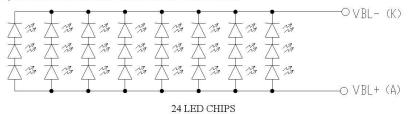
ELECTRICAL CHARACTERISTICS

 $T_0 = 25 \, \circ C$

						1	Ca = 25 °C
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS		3.2	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VCC-VSS	_	2.5	3.3	3.6	V	
LOW LEVEL INPUT VOLTAGE	VIL	70	0	_	0.3*VDD	V	NOTE (1)
HIGH LEVEL INPUT VOLTAGE	VIH	_	0. 7* VDD	_	VDD	V	NOTE (1)
POWER SUPPLY CURRENT	IDD	VDD-VSS = 3.3 V	=	220	270	mA	NOTE (2)
POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC-VSS=3.3V LED B/L=ON		600	780	mA	
DIMMING CONTROL FOR	LEDCTRL-VSS	_	0		3	V	NOTE (3)
LED BACKLIGHT	PWM	1KHz		3.3	-	V	MOTE (3)
LED LIFE TIME	-		30K	40K		HRS	

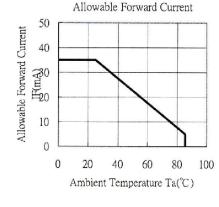
NOTE (1): APPLIED TO TERMINALS B5~B0 , G5~G0 , R5~R0 , DCLK , HSYNC , VSYNC , ENB , ROTATE. NOTE (2): THE DISPLAY PATTERN IS ALL "WHITE"...

NOTE (3): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (4): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT. (PER. LED)

Ambient Temperature vs.





MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ET070080DH6 4 4 TIMING CHARACTERISTICS AC ELECTRICAL CHARACTERISTICS 5.1 PARAMETER SYMBOL MIN. TYP. MAX. UNIT HSYNC SETUP TIME Thst 6 ns HSYNC HOLD TIME Thhd 6 VSYNC SETUP TIME Tvst 6 ns VSYNC HOLD TIME Tvhd 6 ns DATA SETUP TIME Tdsu 6 ns DATA HOLD TIME Tdhd 6 ENB SETUP TIME 6 Tesu ns SOURCE OUTPUT SETTLING TIME $T_{\scriptscriptstyle ST}$ 15 μ s $R_{\scriptscriptstyle \rm SL}$ SOURCE OUTPUT LOADING R 2 K ohm SOURCE OUTPUT LOADING C \mathbf{C}_{SL} 60 pF ENB -**DCLK** (EDGSL=H) DX[7:0] -2nddata **DCLK VSYNC HSYNC**



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	NOLOGIES CORPORATIO		ET070	080	DH6		4	5
5.2	SYNC MODE SIGNAL O	ги арасть	DICTICC			•		
3.2	STING MODE SIGNAL (JHARACIT	KIS HCS					
	PARAMETER		SYMBOL		SPEC		UN	IT
	TAKAMETEK		STWINGL	MIN.	TYP.	MAX.	OIV	1.1
	DCLK FREQUENCY		F_{CPH}	_	33.26		MF	Iz
	DCLK PERIOD		T_{CPH}	-	30.06		ns	3
	DCLK PULSE DUTY		T_{CWH}	40	50	60	%	
	HSYNC PERIOD				1056	1057	T_{CI}	PH
	HSYNC PULSE WIDTH	SYNC PULSE WIDTH			128	_	$T_{\rm CI}$	PH
	HSYNC -FIRST HORIZONTAL :	T_{HS}	<u>= = = = = = = = = = = = = = = = = = = </u>	216		$T_{ m CI}$	PH	
	HSYNC ACTIVE TIME	$T_{\rm HA}$	_	800	_	$T_{ m CI}$	PH	
	VSYNC PERIOD	$T_{\rm V}$		525		T _I	I	
	VSYNC PULSE WIDTH		T_{WV}	1	2	_	T_{I}	H
	VSYNC -DE TIME		$T_{ m VS}$	-	35	_	T _I	H
	VSYNC ACTIVE TIME		T_{VA}		480	_	T	ł
ENB DO[7:0]	Tose	R1 R2 R3 R4	R5 R8	. R	R R	-	nvalid data	
D1[7:0]	Invalid data	G1 G2 G3 G4	G5 G8	. G N-2 N	G G I-1 N	li	nvalid data	
D2[7:0]	Invalid deta	B1 B2 B3 B4	B5 B6 ·····	B N-2 N	B B I-1 N	li	nvalid data	
	▼ T _F z	- 4	T_		-			S.
	SYN	C Mode Hori	izontal Dat	a Form	at			
VSYNC	T _w		TV				74.2	
HSYNC TIT					uщ	┰╬	t	ш
0,01,02[7:0]	Invalid data	DL DL 1	DL	DL DL W-3 M-2	D. DL M-1 M		Invalid data	1
NB -				ЛП	ЛЛĻ			
	■ Twx	-	Twe		-			
	SYNC	Mode Verti	cal Data Fo	ormat				



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A DELICOPE GLOVAL GUADA GERD	Tamtaa					
5.3 DE MODE SIGNAL CHARACTER) m) t	marn	2 (43)	I 131	
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UN	
DCLK FREQUENCY	F _{CPH}		33.26		M	
DCLK PERIOD DCLK PULSE DUTY	T _{CPH}	40	30.06 50	60	ns %	
ENB PERIOD	T_{CWH} $T_{DEH} + T_{DEL}$	1000	1056	1200	T _{Cl}	
ENB PULSE WIDTH	T _{DEH} + 1 _{DEL}	1000	800	1200	T _{CI}	
ENB FRAME BLANKING	T _{DEB}	10	45	110	T _{DEH} +	
ENB FRAME WIDTH	T _{DE}	10	480	110	T _{DEH} +	T_{p}
OEV PULSE WIDTH	T _{OEV}		150		T _{Cl}	
CKV PULSE WIDTH	$T_{\rm CKV}$		133		T _{Cl}	
ENB(INTERNAL)-STV TIME	T ₁		4		T _{Cl}	
ENB(INTERNAL)-CKV TIME	T ₂		40		T _{Cl}	
ENB(INTERNAL)-OEV TIME	T ₃		23	_	T _{Cl}	
ENB(INTERNAL)-POL TIME	T ₄	22	157		T _{CI}	
STV PULSE WIDTH	-4		1		T	
NOTE: (1) $T_{HS}+T_{HA}< T_{H}$		W			1	1
THE	1 Period (1 Frame)				
_	11		_			
TDE		*	Ты	В	→	
	1000000	וחחחחחו	INNNNN	חחחחחח]
	UUUUWUUUUL	UUUUUU	UUUUUUU		ՍՍՍԱՍՍ	L
	- // -	٦				П
ENB	//	Ц				Ш
	//					
	\ <u>\</u>					_
DATA[17:0]					111	
	TÅLTTTAPÍ	<u> </u>			$ \mu$	_
1/	//	1				
У /	<i>V</i>	1	*****			
<i>f</i>			******	·.		
1				****		
/				*****		
/ 18	orizontal Perio	nd		-	*****	
R	orizontal Perio	od		Toro		
1 H	orizontal Perio	od	→	Toel		
Тоен	orizontal Perio			T _{DEL}		1
r4	orizontal Perio			T _{DEL}		
Тоен	orizontal Perio	od		TDEL		
Тоен	orizontal Perio	od		Toel		
Тоен	orizontal Perio	od		Toel		
DOLK TOEH ENB	orizontal Perio			Toel		
DOLK TOEH ENB	lorizontal Perio	N-2 N-1		Toel		
DOLK TOEH	orizontal Perio		N N	Toel		
DCLK ENB DATA[17:0] 1 2 3			N N	Toel		
DOLK TOEH ENB			N N	Toel		
DCLK Toeh DATA[17:0] 1 2 3			N	Toel	1	
DCLK ENB DATA[17:0] 1 2 3			N N	Toel		l
DCLK ENB DATA[17:0] 1 2 3			N N	TDEL.		
DCLK ENB DATA[17:0] 1 2 3			N N	TDEL.		
DCLK TDEH DATA[17:0] 1 2 3			N N	TDEL.		∏_ - -



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6. OPTICAL CHARACTERISTICS (NOTE1)

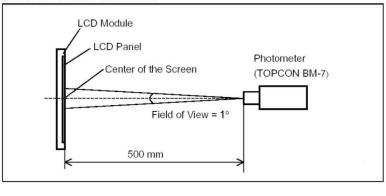
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

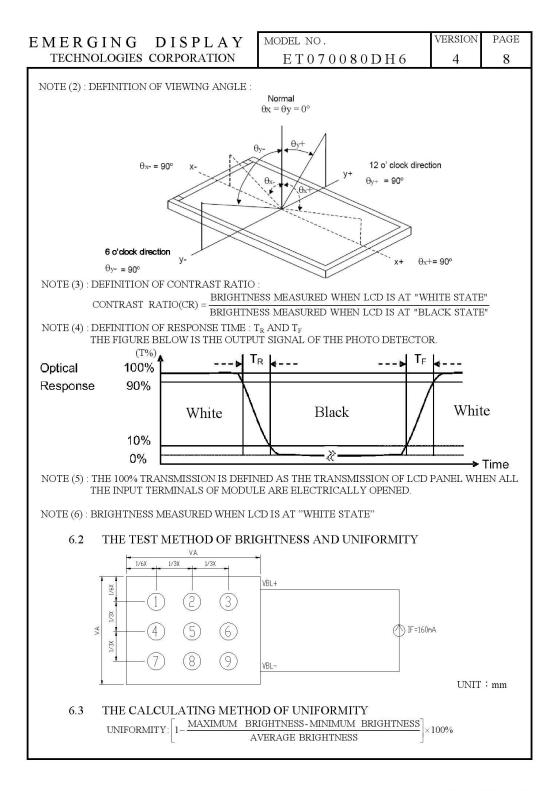
200 DO 0000 -	000 0	NAME OF THE OWNER.			10 10000 100	X.326(34800.5)	Part decreases		= 25 ± 2 °C
ITE	M	SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK
		Θ_{y+}		θ*=0 ₀	60	65			
VIEWING ANGLE		Θ_{y}	CR ≥ 10	θ_{x} =0	60	65	-	disco	NOTE (2)
		θ_{x^+}	CK 2 10	0 -00	65	70		deg.	NOTE (3)
		θ_{x}		θ _y =0°	65	70	-		
CONTRAST RAT	CIO	CR	θx=0°,	θу=0°	300	350			NOTE (3)
RESPONSE TIM	D	T _R (rise)	θx=0°, θy=0°		_	5	10		NOTE (4)
KESPONSE IIVI	D.	T _F (fall)			.—.	15	20	msec	
	WHITE	Wx			0.26	0.31	0.36	_	NOTE (5)
		Wy	θx=0°, IF = 160 (NTSC :		0.29	0.34	0.39		
GOLOD OF	RED	Rx		1	0.51	0.56	0.61		
COLOR OF CIE		RY			0.29	0.34	0.39		
COORDINATE	GDEEN	Gx			0.28	0.33	0.38		
COORDINATE	GREEN	Gy		3 35 332	0.53	0.58	0.63		
	DIIII	Bx			0.10	0.15	0.20		
BLUE -		Ву			0.07	0.12	0.17	_	
THE BRIGHTNESS OF MODULE		В			250			cd/m ²	
		В	θx=0°,	θy=0°	230			cu/m	NOTE (6)
THE UNIFORM	TY OF		IF = 1	60mA	75	80		%	NOIE (6)
MODULE					15	60		70	

NOTE (1): TEST EQUIPMENT SETUP:

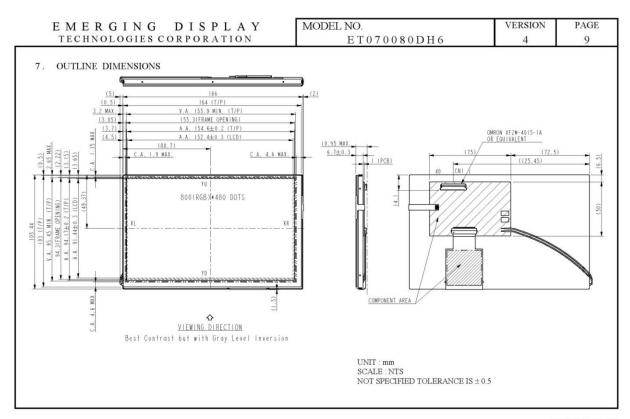
AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF $50 \, \mathrm{cm}$ AND NORMAL DIRECTION.



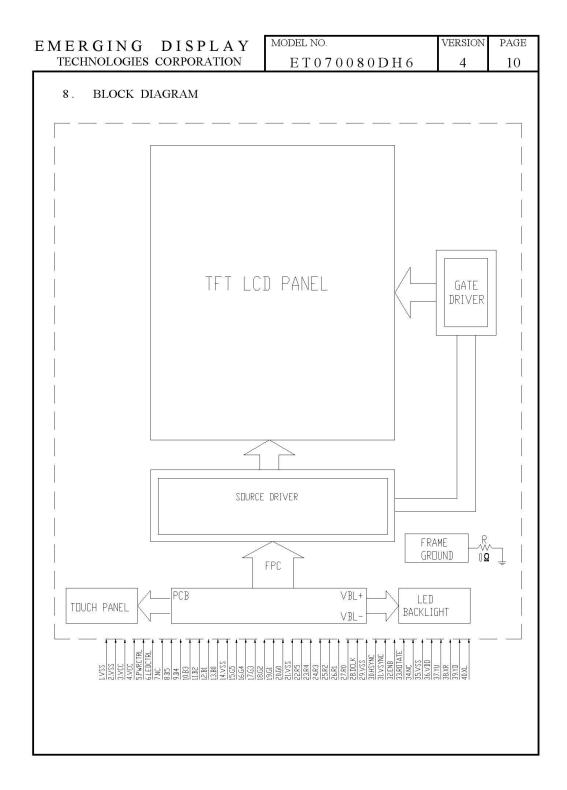




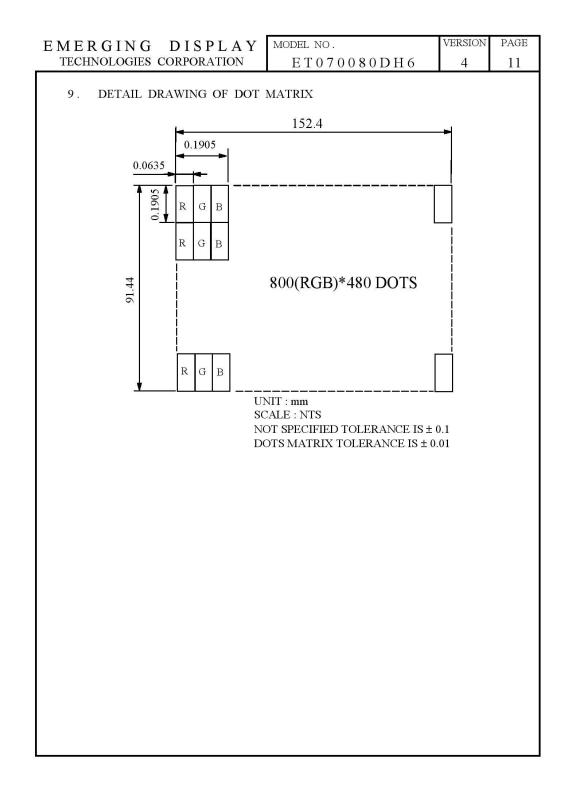














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10. INTERFACE SIGNALS

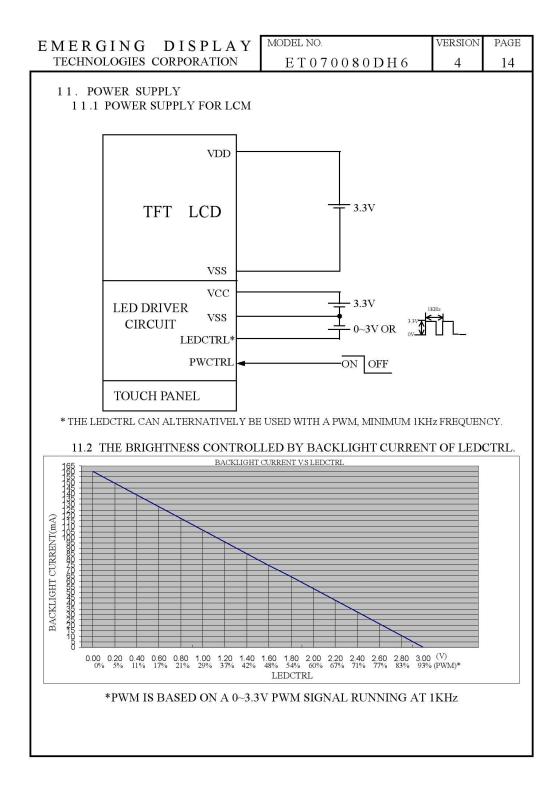
PIN NO	SYMBOL	I/O	FUNCTION				
1	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH				
2	VSS	P	CONDUCTIVE TAPE)				
3	VCC	Р	POWER SUPPLY FOR LED DRIVER CIRCUIT				
4	VCC	P	POWER SUPPLY FOR LED DRIVER CIRCUIT				
			PWCTRL REMARK LOGIC H POUNT ON				
5	PWCTRL	I	LEVEL H POWER ON				
L'			L=0V L SHUTDOWN				
6	LEDCTRL	I	BRIGHTNESS CONTROL FOR LED BACKLIGHT :				
			POWER SUPPLY 0~3V OR PWM SIGNAL				
7	NC		NON CONNECTION				
8	В5	I	BLUE DATA BIT 5				
9	B4	I	BLUE DATA BIT 4				
10	В3	I	BLUE DATA BIT 3				
11	B2	I	BLUE DATA BIT 2				
12	B1	I	BLUE DATA BIT 1				
13	В0	I	BLUE DATA BIT 0				
14	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)				
15	G5	I	GREEN DATA BIT 5				
16	G4	I	GREEN DATA BIT 4				
17	G3	I	GREEN DATA BIT 3				
18	G2	I	GREEN DATA BIT 2				
19	G1	I	GREEN DATA BIT 1				
20	G0	I	GREEN DATA BIT 0				
21	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)				
22	R5	I	RED DATA BIT 5				
23	R4	I	RED DATA BIT 4				
24	R3	I	RED DATA BIT 3				
25	R2	I	RED DATA BIT 2				
26	R1	I	RED DATA BIT 1				
27	R0	I	RED DATA BIT 0				
28	DCLK	I	DOT DATA CLOCK				
29	VSS	Р	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)				



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PIN NO	SYMBOL	I/O	FUN	NCTION		
30	HSYNC	I	HORIZONTAL SYNC INPUT. INTERNALLY PULL HIGH.	DE & SYNC MODE SETTING DE MODE : ONLY ENB AND PIXEL		
31	VSYNC	I	VERTICAL SYNC INPUT. INTERNALLY PULL HIGH.	CLOCK IS NECESSARY. SYNC MODE : ENB SHOULD BE CONNECTED TO VSS.		
32	ENB	I	DATA ENABLE INPUT. INTERNALLY PULL LOW.	HSYNC/VSYNC AND PIXEL CLOCK IS NECESSARY.		
33	ROTATE	1	WHEN ROTATE="H" THEN UD="H" LEVEL: OUT1→480 LR="L" LEVEL: OUT800→1 WHEN ROTATE="L" THEN UD="L" LEVEL: OUT480→1 (DEFAULT) LR="H" LEVEL: OUT1→800 (DEFAULT)			
34	NC	-	NON CONNECTION			
35	VSS	P	GROUND (VSS IS CONNECTED TONDUCTIVE TAPE)	TO METAL HOUSING WITH		
36	VDD	P	POWER SUPPLY VOLTAGE			
37	YU	_	TOP PANEL			
38	XR		RIGHT PANEL	TOUCH PANEL		
39	YD		BOTTOM PANEL	TOUCHFANEL		
40	XL	-	LEFT PANEL]		





 $M\Omega$



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12. TOUCH PANEL SPECIFICATION

12.1 ELECTRICAL CHARACTERISTICS					
ITEM	CONDITION	SPEC.	UNIT		
LINEARITY	A	< 1.5	%		
TRANSMISSION	ASTM D1003	84±3	%		
ACTIVATION FORCE	SILICON " FINGER"	10~80	g		
TERMINAL RESISTANCE	X AXIS	400 ~ 1200	Ω		
TERMINAL RESISTANCE	Y AXIS	$100 \sim 600$	22		

DC25V

> 10

INSULATION RESISTANCE 12.2 ABSOLUTE MAXIMUM RATINGS:

	3	to: to	0/
ITEM	MIN.	TYP.	MAX.
OPERATING TEMPERATURE (Top)	-30°C		80°C
STORAGE TEMPERATURE (Tst)	-40°C	_	80°C
INPUT VOLTAGE (V)	_	5	5.5

12.3 PRECAUTIONS IN USE OF TOUCH PANEL

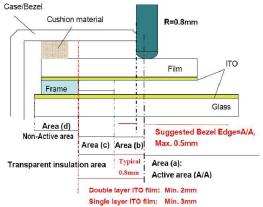
12.3.1 PURPOSE:

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.3.2 ITEM AND ILLUSTRATION:

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW:



THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT

UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET

CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.



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AREA(a): ACTIVE AREA

THE ACTIVE AREA IS GUARANTEED THE POSITION DATA DETECTABLE PRECISION, OPERATION FORCE AND OTHER OPERATIONS. IT IS STRONGLY RECOMMENDED TO PLACE THE OPERATION BUTTON OR MENU KEYS WITHIN THE ACTIVE AREA. DUE TO STRUCTURE, THE ACTIVE AREA IS LESS DURABLE AT THE EDGE OR CLOSE TO THE EDGE.

AREA(b): OPERATION NON-GUARANTEED AREA

THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

AREA(c): PRESSING PROHIBITION AREA

THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

AREA(d): NON-ACTIVE AREA
THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.

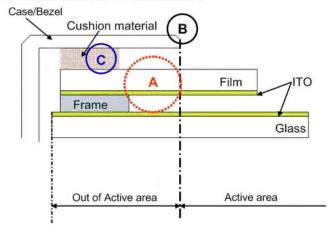
- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING
 - (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.
 - (ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC(FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
 - (iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.
 - (iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY (REFER TO ITEM 5.1.2. STRUCTURE, AREA DEFINITION).

BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA, IT MAY CAUSE THE DAMAGE OF THE ITO FILM.



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(v) PRESSING INSIDE OF BOUNDARY OF THE FRAME(PART (A) AS SHOWN IN BELOW) MAY CAUSES FAULT OPERATION, SO PLEASE DESIGN TO AVOID PRESSING OF TOUCH PANEL AT PART (A) SUCH AS HAVING GASKET/CUSHION AT PART (C). PARTICULARLY THE AREA (B) SHALL BE FREE FROM BURR. THE GASKET/CUSHION MATERIAL AT THE PART (C) SHOULD NOT BE EXCEEDED TO INSIDE OF THE BOUNDARY OF THE FRAME.



- (vi) TO PREVENT GIVING DISTORTION TO THE FILM OF THE PRODUCT AND PEELING OFF OF THE FILM FROM THE PRODUCT, DO NOT FIX THE FILM AND A SET CASE OR A SHOCK ABSORBING MATERIAL ADHERED TO A SET CASE BY ADHESION.
- (vii) WIPE OFF THE STAIN ON THE PRODUCT BY USING SOFT CLOTH MOISTENED WITH ETHANOL. TAKE CARE NOT TO ALLOW ETHANOL TO SOAK INTO THE JOINT OF UPPER FILM AND BOTTOM GLASS. IT MAY OTHERWISE CAUSE PEELING OR DEFECTIVE OPERATION. DO NOT USE ANY ORGANIC SOLVENT OR DETERGENT OTHER THAN ETHANOL.
- (viii) THE CORNERS OF THE PRODUCT ARE NOT CHAMFERED AND ARE SHARP. WHEN POSITIONING AND FIXING THE PRODUCT ON THE CASE, PROVIDE A ROUND PART ON THE CORNER OF THE CASE SO AS NOT TO APPLY LOAD ON THE CORNER OF THE TRANSPARENT TOUCH PANEL.
- (ix) DO NOT PRESS THE FILM OF THE PRODUCT WHEN THIS PRODUCT IS BUILT INTO A SET.
- (3) CAUTIONS FOR OPERATION
 - (i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHOSE TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.



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- (ii) THE INPUT POSITION MAY BE FLUCTUATED A LITTLE THROUGH LONG-TIME USE. IT IS DESIRABLE TO PROVIDE A ZERO-ADJUSTMENT FUNCTION BY USING A CIRCUIT AND SOFTWARE.
- (iii) OPERATION AT THE OUT OF ACTIVE AREA IS OUT OF OUR GUARANTEE. IT CAUSES A SERIOUS DAMAGE OF A TRANSPARENT ELECTRODE. DO NOT OPERATE AT THE OUT OF ACTIVE AREA.
- (iv) IN CASE OF CLEANING THE PART OF THE CASE BOUNDARY OF ACCOMPLISHED SET, USE A SOFT CLOTH WITH A FINGER BERRY OR A COTTON BUD. DO NOT CLEAN WITH A THI NG OTHER THAN THE FINGER SUCH AS HARD OR SHARP EDGES LIKE A FINGER NAIL ETC. ON THE CLOTH, BECAUSE IT CAUSE TRANSPARENT CONDUCTIVE FILM CRACKS. PLEASE ADVISE THIS PROHIBITION TO YOUR LAST CUSTOMERS.

12.4 DURABILITY

12.4.1 STYLUS HITTING:

ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFACE PEN: R8 mm SILICON RUBBER

LOAD: 250g

FREQUENCY: 240 times/min MEASUREMENT POSITION:

1 POINT OF TOUCH PANEL ACTIVE AREA REPEATED: OVER 1,000,000 TIMES

12.4.2 PEN TOUCH SLIDING DURABILITY:

100,000 TIMES OR OVER WRITING WITH R0.8mm PLASTIC STYLUS PEN; WRITING FORCE 150g IN ACTIVE AREA. SPEED IS 60mm/sec.



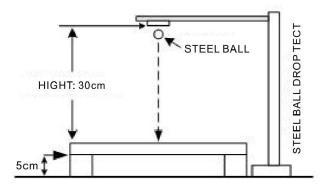
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12.5 STEEL BALL DROP TEST

BY USING F9mm STEEL BALL FROM THE HEIGHT OF 30cm AND FALLING ON TOUCH PANEL SURFACE, MUST PASS BELOW CONDITIONS:

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING THE PANEL BROKEN.



12.6 APPEARANCE INSPECTION

PURPOSE:

TO ESTABLISH APPEARANCE STANDARD AND MAINTAIN PRODUCT QUALITY \circ

SCOPE:

TOUCH PANEL VIEW AREA WITHIN TOUCH PANEL •

12.6.1 RULE:

INSPECTION CONDITION

- (A) ENVIRONMENTAL LUMINANCE: 500 LUX °
- (B) DISTANCE BETWEEN HUMAN EYES AND PANEL: 30 CM (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT) °
- (C) VISUAL ANGEL: > 60° °
- (D) LIGHT SOURCE: FLUORESCENT LIGHT SOURCE •

12.6.2 JUDGE CRITERION:

JUDGEMENT UNDER ABOVE MENTIONED CRITERION (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT),

TESTING GOODS DEFECT CAN BE VISIBLE WITHIN 10 SECONDS, WHICH WILL BE JUDGED AS MAJOR DEFECTS \circ

SAMPLING STANDARD:

THE SAMPLING STANDARD WILL BE CONFIRMED BY BOTH OF EDT AND CUSTOMER.



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DEFECT ITEM	SF	PECIFICATIONS	ALLOWED /REJECT	REMARK
		D ≤ 0.10	NOT COUNT AS A DEFECT	
OPAQUE SPOT		$0.10 < D \le 0.30$	ACCEPT	SEE REMARK
		D > 0.30	REJECT	
HOLLOW OR		$D \le 0.4$	ACCEPT	
PROTUBERANCE SPOT		D > 0.4	REJECT]
	I INABILITAMEASIDE I		NOT COUNT AS A DEFECT FOR ANY	
	WIDTH		LENGTH	
SCRATCH	$W \le 0.025$		$L \leq 20$	SEE REMARK
	$0.025 < W \le 0.05$		$L \leq 10$	
	W > 0.05		REJECT	
LINT (FIBROUS		$W \leq 0.025$	$L \leq 10$	SEE REMARK
MATERIAL, HAIR,	0	$.025 < W \le 0.05$	$L \le 5$	SEE REMARK
ADHESIVE, LINT)		W > 0.05	REJECT	
CHIP ON GLASS	$ \begin{array}{c} \text{CORNER} & X \leq 1.0\text{mm AND} \\ \text{Y} \leq 1.0\text{mm AND} \\ \text{Z=GLASS THICKNESS} \\ \\ \text{EDGE} & X \leq 6.0\text{mm AND} \\ \text{Y} \leq 2.0\text{mm AND} \\ \text{Z} < \text{GLASS} \\ \text{THICKNESS} \\ \end{array} $			x y z
			ACCEPT	x 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2

D: DIAMETER; W: WIDTH; L: LENGTH

UNIT: mm

REMARK:

- IF THE DISTANCE BETWEEN DEFECTS IS < 10 mm, THE PRODUCT SHALL BE REJECTED. IT IS ACCEPTED IF THE DISTANCE BETWEEN DEFECTS \geq 10 mm.
- •THE ABOVE DEFECT SPECIFICATIONS ARE DEFINED IN THE ACTIVE AREA. IF THERE IS ANY DEFECT THAT IS BLACK OR COLORED LINT OR DOT LOCATED IN THE VIEWING AREA, IT SHALL BE DEFINED AS THE ACTIVE AREA SPECS. FOR TRANSPARENT OR TRANSLUCENT TYPE OF DEFECT LOCATED AT NON-ACTIVE AREA IS ACCEPTABLE IF ITS DIAMETER IS LESS THAN 0.5mm.



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13. INSPECTION CRITERION

13.1 APPLICATION

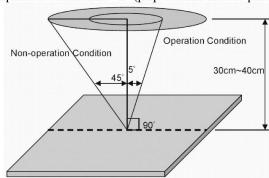
This inspection standard is to be applied to the LCD module delivered from EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) to customers

13.2 INSPECTION CONDITIONS

13.2.1 (1)Observation Distance: 35cm±5cm

(2) View Angle:

Non-operation Condition: ±5°(perpendicular to LCD panel surface) Operation Condition: ±45° (perpendicular to LCD panel surface)



13.2.2 Environment Conditions:

Ambi	ent Temperature	20°C~25°C
Am	bient Humidity	65±20%RH
Ambient	Cosmetic Inspection	More than 600Lux
Illumination	Functional Inspection	300~500 Lux

13.2.3 Inspection lot

Quantity per delivery lot for each model

13.2.4 Inspection method

A sampling inspection shall be made according to the following provisions to judge The acceptability

(a)Applicable standard : MIL-STD-105E

Normal inspection, single sampling

Level II

(b)AQL : Major defect : AQL 0.65 Minor defect : AQL 1.0



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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	



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13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM		CRI'	TERIA	
1.	DISPLAY ON INSPECTION	(1)INCORRECT P (2)MISSING SEGI (3)DIM SEGMEN' (4)OPERATING V	MENT	O SPEC	
2.	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC			
3.	DOT DEFECT	AND BLUE SO (2) BRIGHT DOT DARK DOT TOAL BRIGHT NOTE: 1. THE DEFINITIO THE SIZE OF A REGARDED AS 2. BRIGHT DOT: DOTS APPEAR PANEL IS DISP 3. DARK DOT:	TEMS TEMS AND DARK DOTS ON OF DOT: DEFECTIVE DOT ONE DEFECTUVE BRIGHT AND UNCLAYING UNDER E	OVER 1/2 OF WHOLI E DOT. CHANGED IN SIZE IN	NT BOOT IS
	FOREIGN	PANEL IS DISP LENGTH: L	LAYING UNDER F WIDTH: W	PURE RED, GREEN, B PERMISSIBLE NO.	
4. BRIGHT LINE/ SCRATCH		$ \begin{array}{c c} L \le 0.3 \\ \hline 0.3 < L \le 2.5 \\ \hline 2.5 < L \\ \hline WIDTH: W mm \end{array} $	$W \le 0.05$ $0.05 < W \le 0.1$ 0.1 < W	IGNORE 4 NONE	
5.,	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	D ≤ 0.15 <	METER (mm): D 0.15 D ≤ 0.5 < D	NUMBER OF PIECES IGNORE 4 NONE	



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NO.	ITEM	-	CRITERIA	
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED
		DIEDDIE ON THE	D ≤ 0.25	IGNORE
		BUBBLE ON THE POLARIZER	0.25 < D ≤ 0.5	N ≤ 5
		FOLARIZER	0.5 < D	NOTE
		SURFACE STATUS	D < 0.1 mm	IGNORE
		BORFACE STATES	$0.1 < D \leq 0.3 mm$	$N \leq 3$
		CF FAIL / SPOT	D < 0.1 mm	IGNORE
		CI TIME DI OI	$0.1 < D \leq 0.3 mm$	$N \leq 3$
6. POLARIZER /DIRT/CF FAIL /SURFACE STAINS		BUBBLE APPEARS (2)THE EXT OBSERV (3)THE DEF AS FOLL	VE DISPLAY AREA. THE ISHALL BE IGNORED IF TEST ON THE OUTSIDE OF ACTRANEOUS SUBSTANCE IST OF A CONTRACT OF A VERAGE DESTRAINED OF A VERAGE DESTRAINED OF A VERAGE DESTRAINED OUTSIGNED OUT	HE POLARIZER BUBBLE TIVE DISPLAY AREA. S DEFINED AS IT CAN B IS POWER ON.
	LINE DEFECT ON		a	
7.	DISPLAY	COSSISTENCE WEST AND SECURITY OF THE COSSISTENCE OF	L OR HORIZONTAL LINE	
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
10.	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.		
11	РСВ	THE SEAL AREA THAN THREE PLA (2)NO OXIDATION COMPARTS ON PCB MCHARACTERISTIC THERE SHOULD EPARTS.	OR CONTAMINATION PCE UST BE THE SAME AS ON	E SHOULD BE NO MORE B TERMINALS. I THE PRODUCTION SSING PARTS OR EXCES



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(2)INSUFFICENT SOLDER (3)LIC (3)LIC (3)LIC (3)LIC (3)LIC (4) POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OF "HEEL" OF LEAD AND PAD SOLDER FILLET (5)CHIP COMPONENT (6)CHIP COMPONENT (7) SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET (7) SOLDER FILLET (8) SOLDER FILLET (9) SOLDER FILLET (1) SOLDER FILLET (2) SOLDER FILLET (3) SOLDER FILLET (3) SOLDER FILLET (4) SOLDER FILLET (5) SOLDER FILLET (6) SOLDER FILLET (7) SOLDER FILLET (1) SOLDER FILLET (1



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NO.	ITEM	CRITERIA
12.	SOLDERING	(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
		(4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13.	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGEI USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14.	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.



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NO.	ITEM	CRITERIA
		THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE GENERAL GLASS CHIP: $\begin{array}{c ccccccccccccccccccccccccccccccccccc$
15. CI	RACKED GLASS	CORNER PART : $\begin{array}{c ccccccccccccccccccccccccccccccccccc$
		a b c st slikx sl slikx sl *X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH DIF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MI REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS DIF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED



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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
ì	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°c FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HUMIDITY TEST	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -30°C FOR 30 MINUTES ~ +80°C FOR 30 MINUTES
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 13.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	DEFED TO SDECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST		AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE



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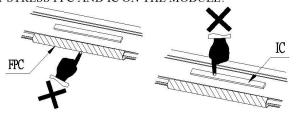
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13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST.
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE .

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM .
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!





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13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
 WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC.
 WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.



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