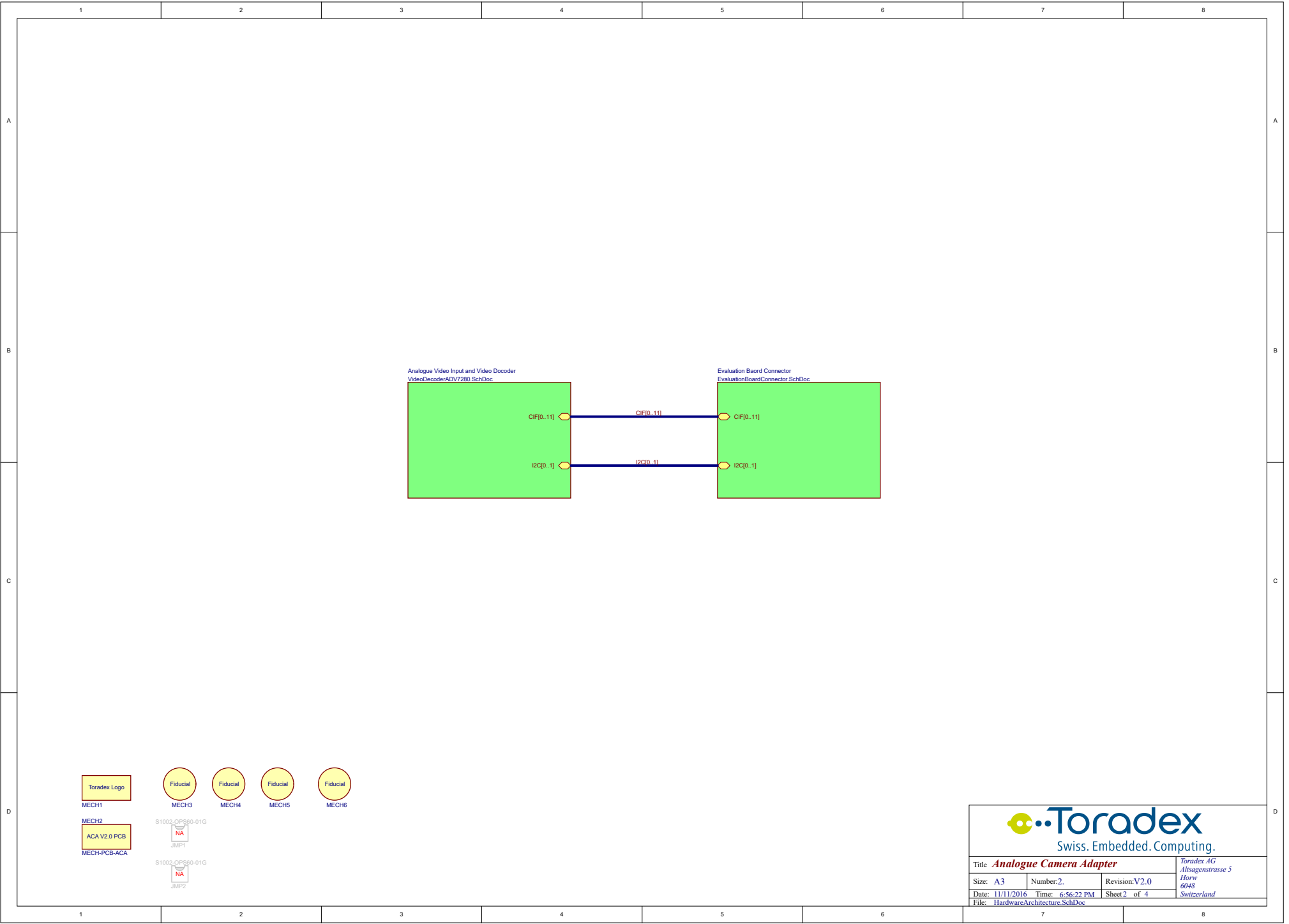


	1	2	3	4	5	6	7	8	
A	<div>REVISION HISTORY</div> <div>1. Design Revision V2.0 : Preliminary design. Date: 29th September 2015</div> <div>2. VideoDecoderADV7280 schematic page: - Changed resistors (R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32 and R33) value from 47 ohm to 22 ohm. Date: 11th November 2016</div> <div>IF IN DOUBT ASK</div>							<div>Project Hardware Architecture HardwareArchitecture_SchDoc</div> <div></div>	A
B								B	
C								C	
D								D	
	1	2	3	4	5	6	7	8	



Title <i>Analogue Camera Adapter</i>			Toradex AG Altsagenstrasse 5 Horw 6048 <i>Switzerland</i>
Size: A3	Number: 1.	Revision: V2.0	
Date: 11/11/2016	Time: 6:56:21 PM	Sheet 1 of 4	
File: RevisionHistory_SchDoc			



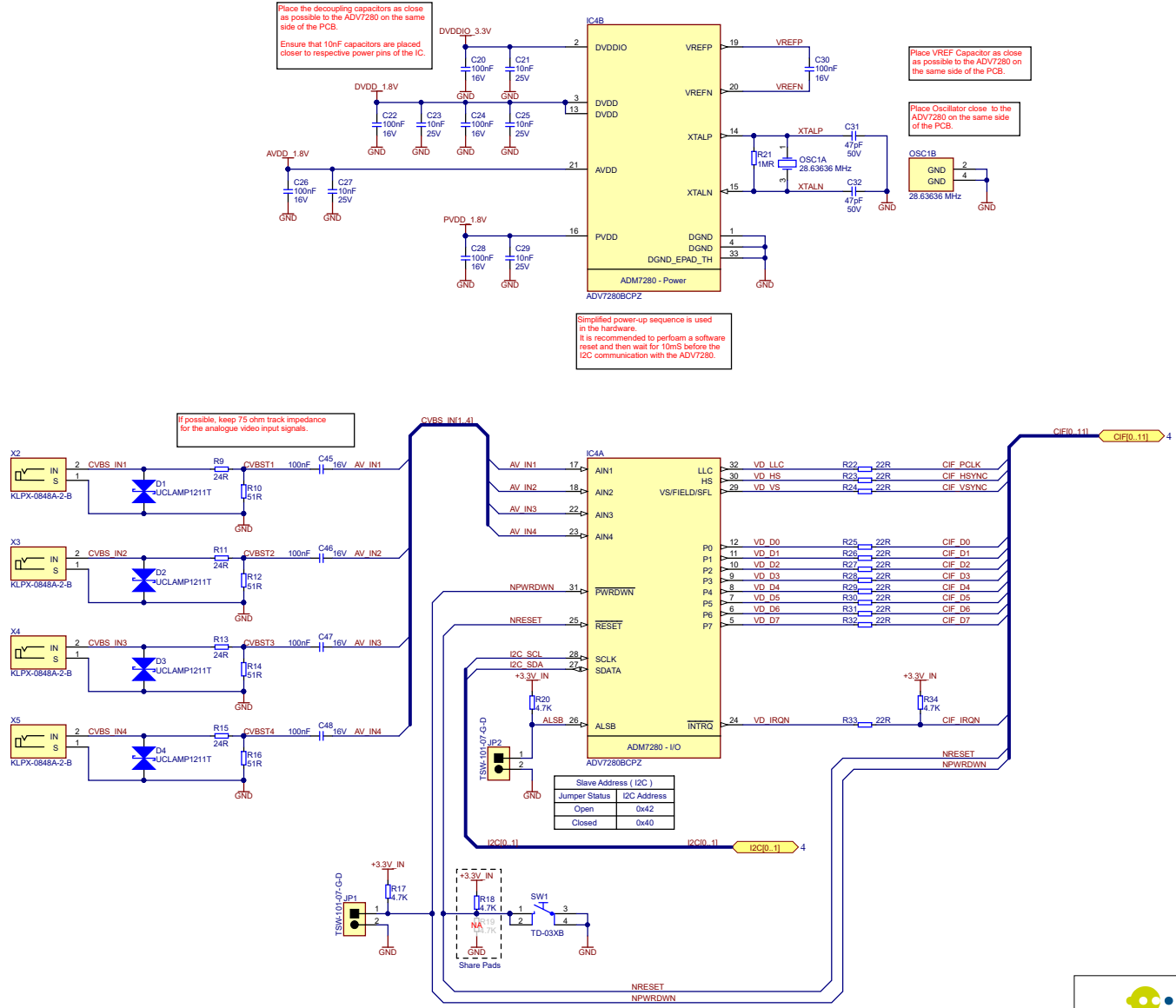
Place the decoupling capacitors as close as possible to the ADV7280 on the same side of the PCB.
Ensure that 10nF capacitors are placed closer to respective power pins of the IC.

Place VREF Capacitor as close as possible to the ADV7280 on the same side of the PCB.

Place Oscillator close to the ADV7280 on the same side of the PCB.

Simplified power-up sequence is used in the hardware.
It is recommended to perform a software reset and then wait for 10mS before the I2C communication with the ADV7280.

If possible, keep 75 ohm track impedance for the analogue video input signals.



Title <i>Analogue Camera Adapter</i>			Toradex AG Altsagenstrasse 5	
Size: A3	Number:3.	Revision:V2.0	Horn 6048	
Date: 11/11/2016	Time: 6:56:22 PM	Sheet 3 of 4	Switzerland	
File: VideoDecoderADV7280.SchDoc				

