


Layer Stackup	 Toradex Swiss. Embedded. Computing.
Tianma 3.5" Display Adapter V1.0.001	


Layer Stackup

Tianma 3.5" Display Adapter V1.0.001

Purpose:	Describes the thickness of each layer used for the project mentioned above
-----------------	--

Doc Revision:	1.0
----------------------	-----

Revision History		
Date	Version	Remarks
29 th Sept 2014	1.0	Initial stackup proposed by Toradex

Layer Stackup	 Toradex Swiss. Embedded. Computing.
Tianma 3.5" Display Adapter V1.0.001	

1 Layer Stackup

This is the stackup initially proposed by Toradex for the Tianma 3.5" Display Adapter Board.

Please ask Toradex if the thickness of layers need to be adjusted.

1.1 Layer thickness

The total thickness of the PCB must be **1.6mm±0.1mm**.

Layer	um	PTH	Description
			soldermask 1
1	signal + diff pair		GTL
			Core
2	signal + diff pair		GBL
			soldermask 2

1.2 Impedance Controlling

The Impedance does not need to be controlled.